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056789-06201

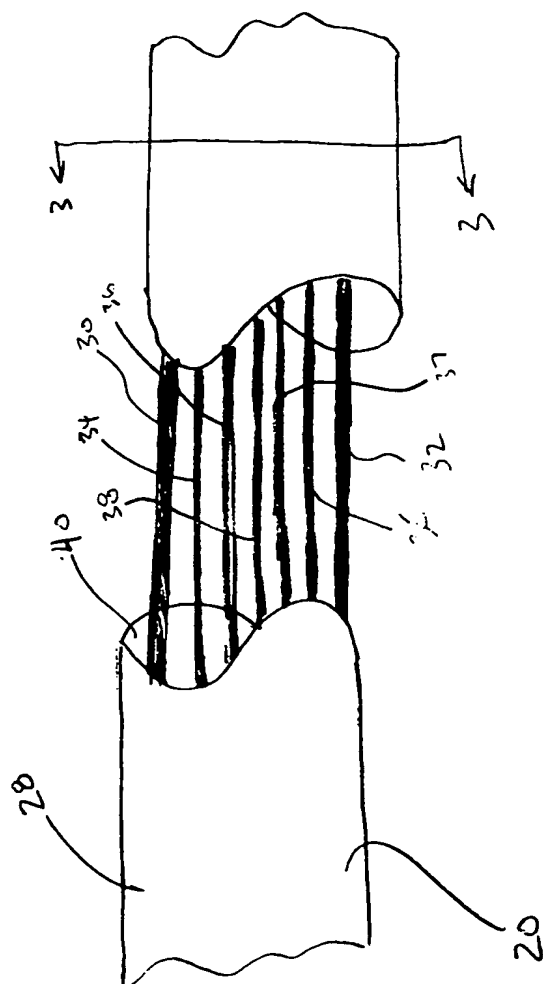
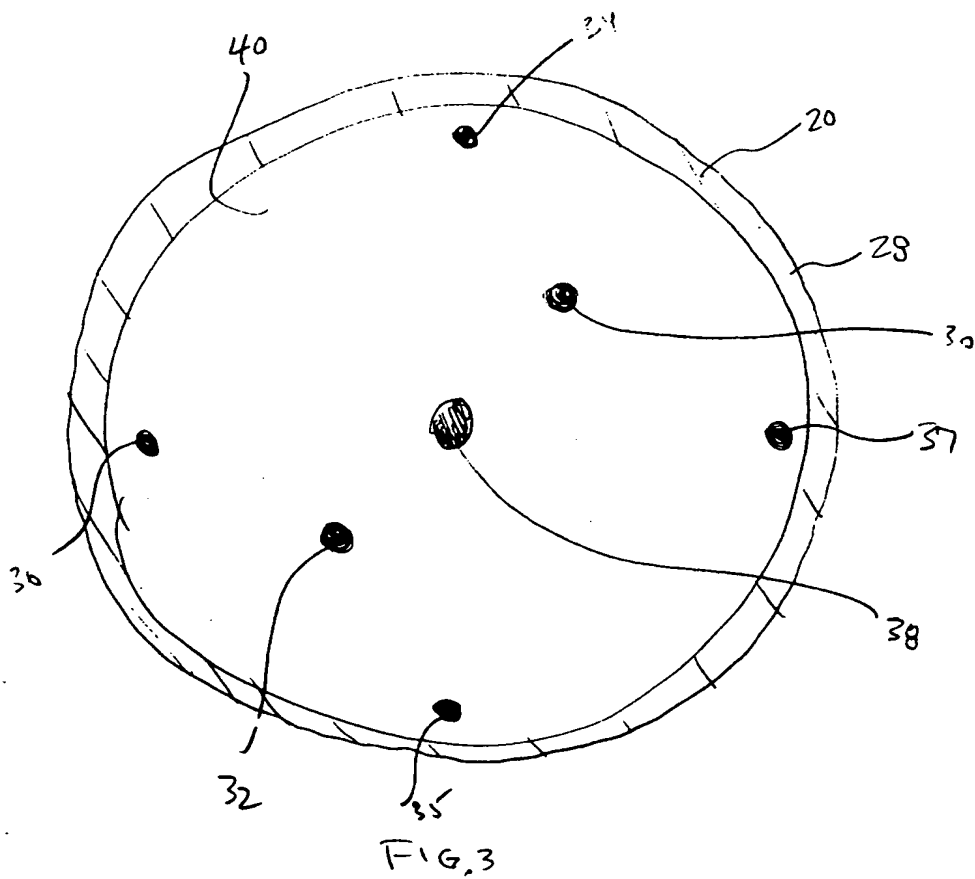
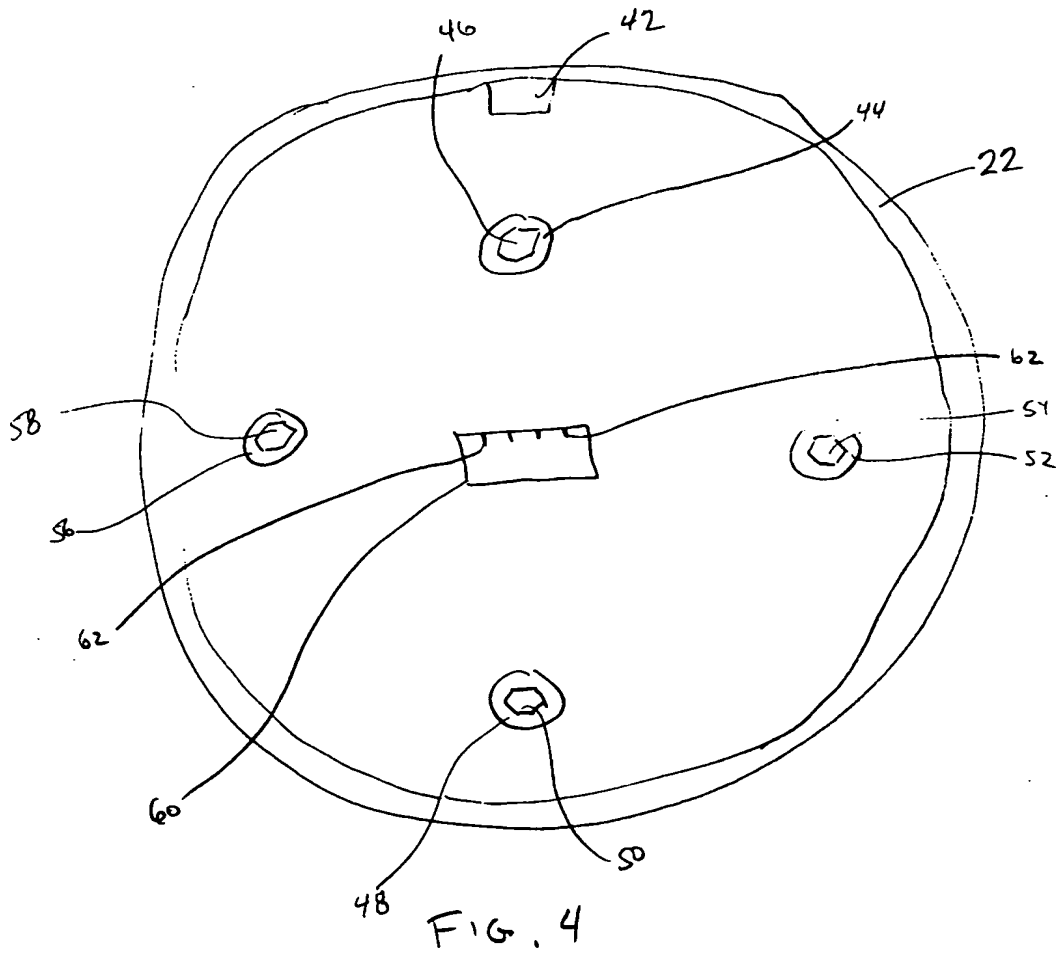


Fig. 2

09887789-052201



090789 062201



09887789 062201

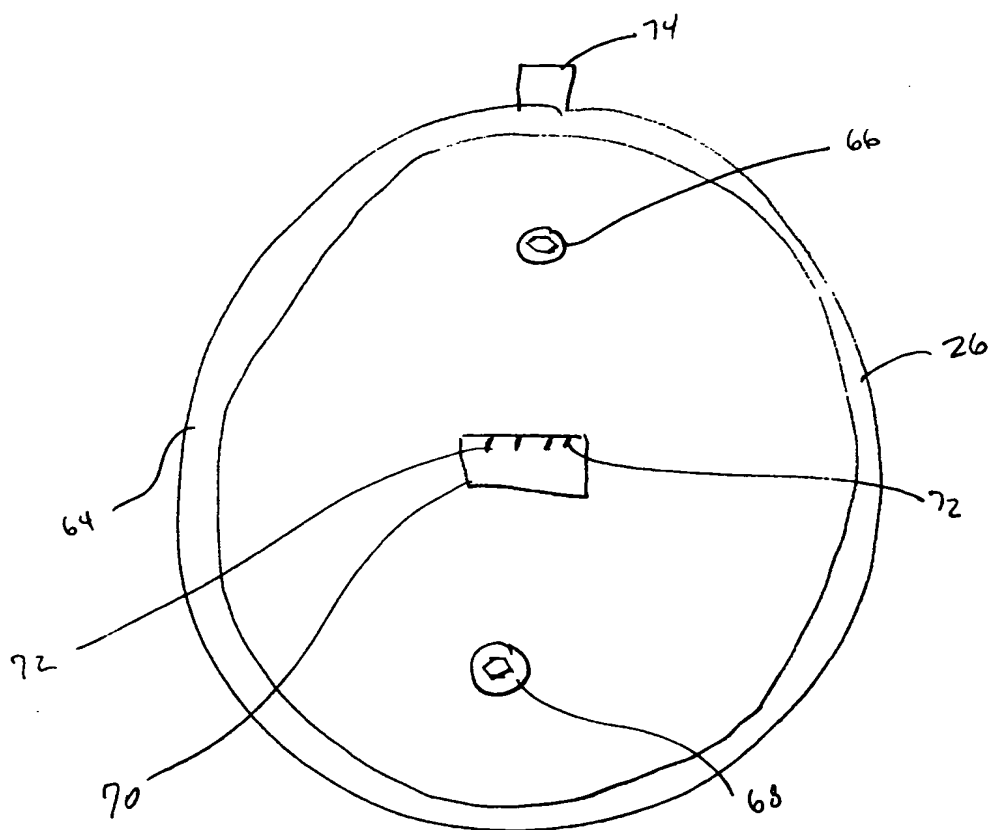


FIG. 5

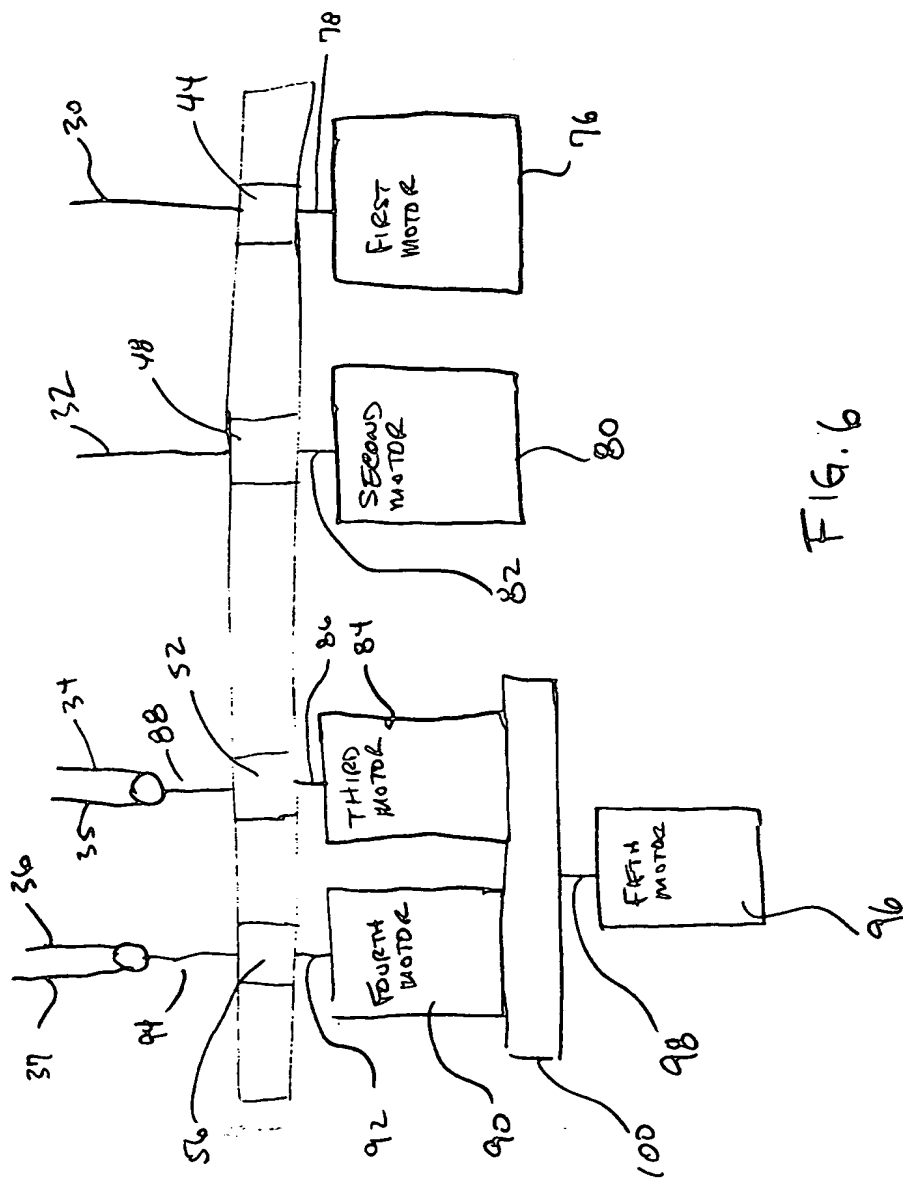


FIG. 6

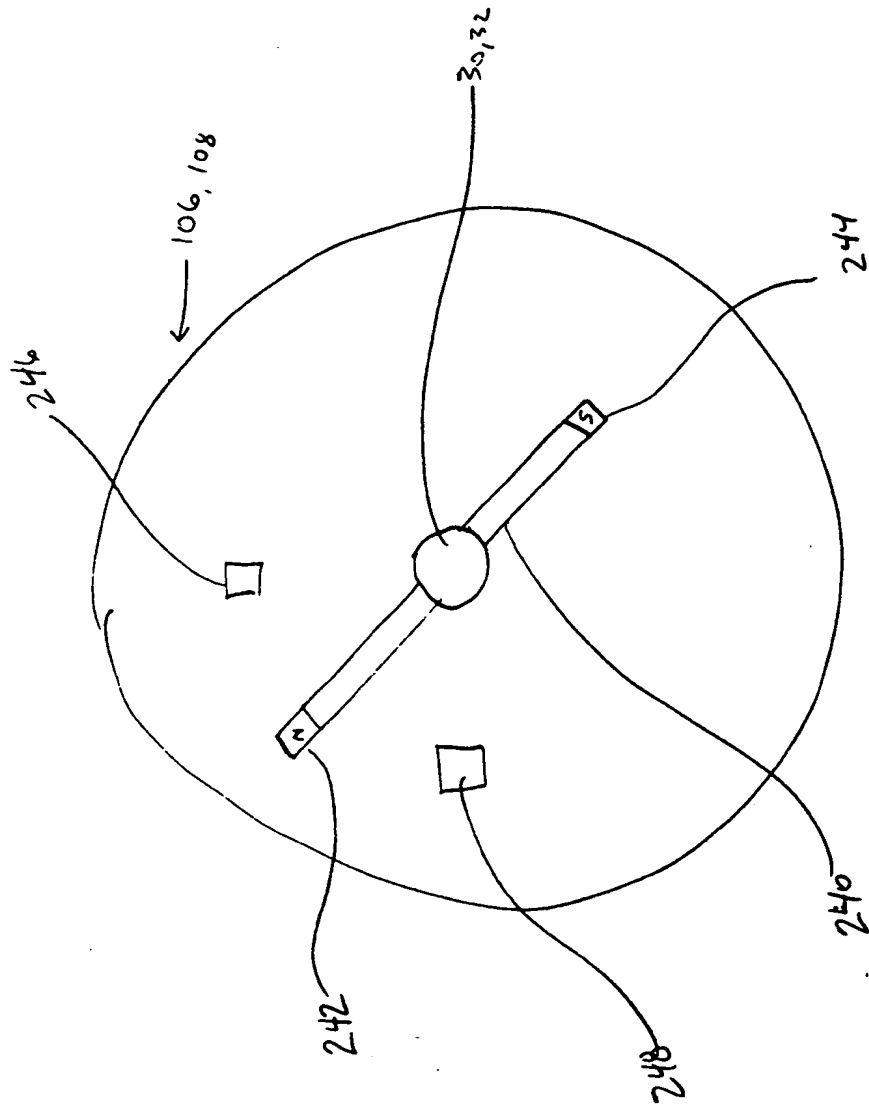


FIG. 8

0588789-062201

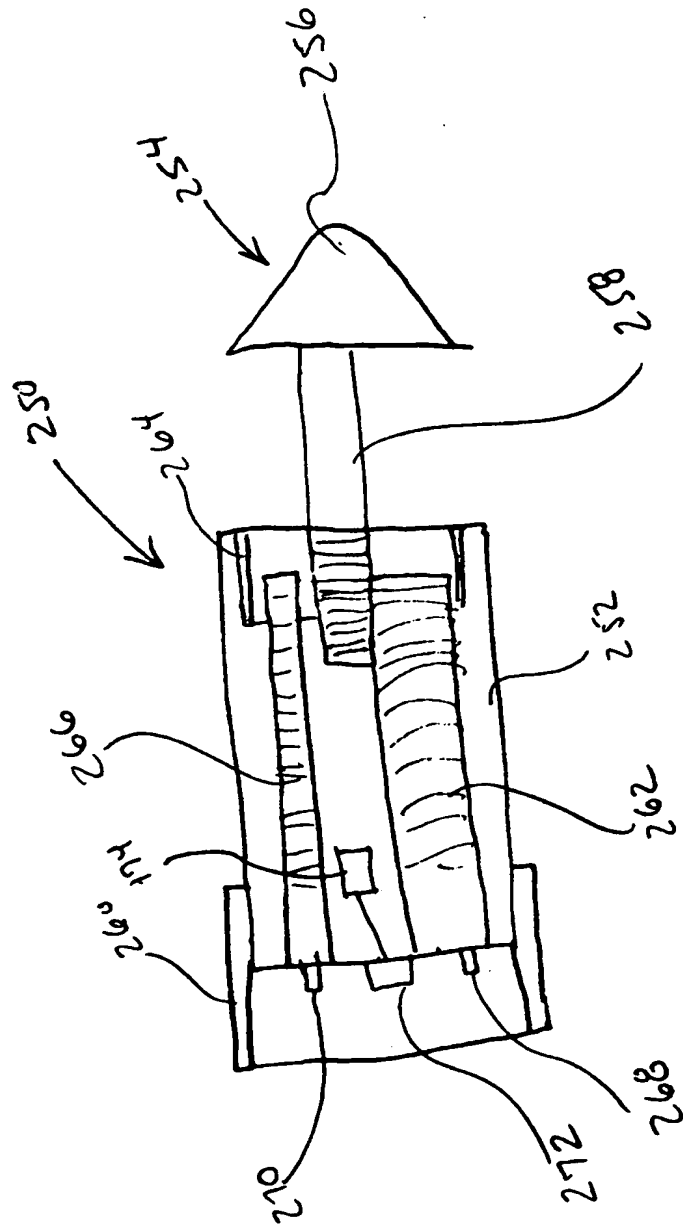


Fig. 9A

[illegible]

Figure 9B

09887789, 062201

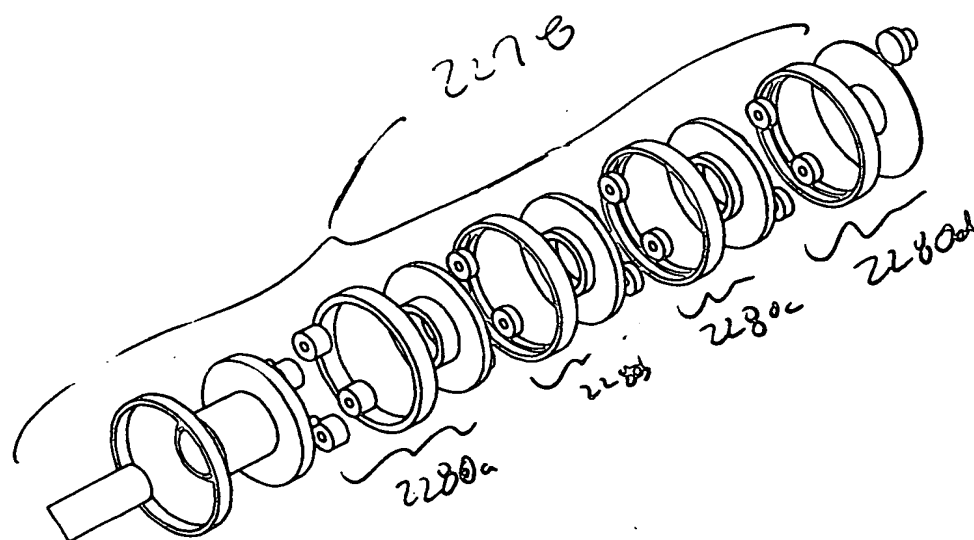


FIG. 9C

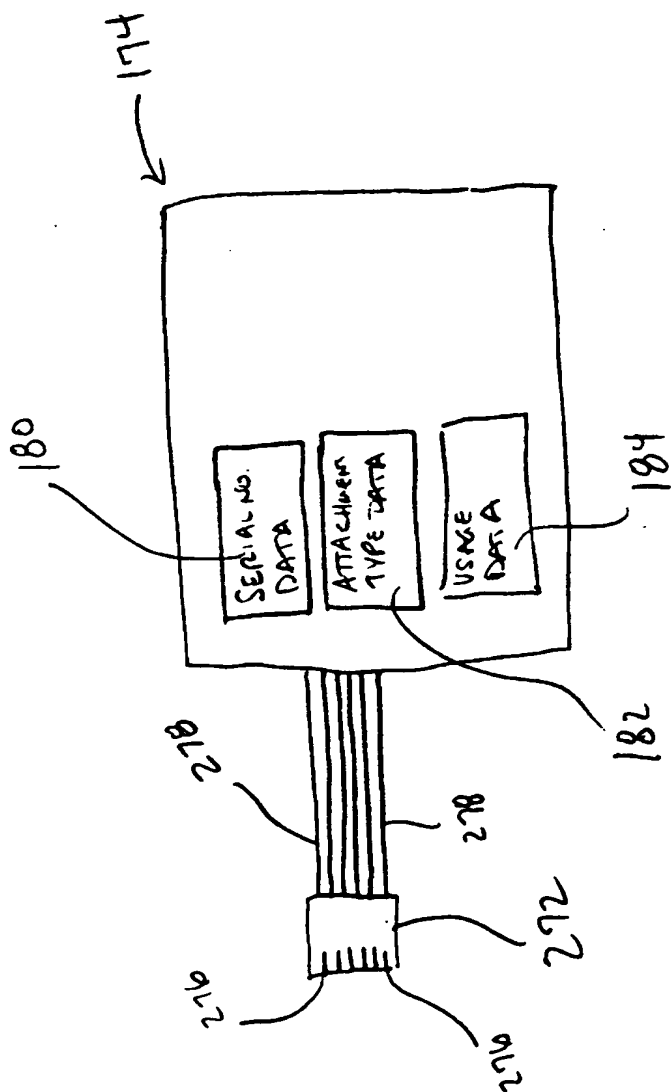


FIG. 10

FIG. 12

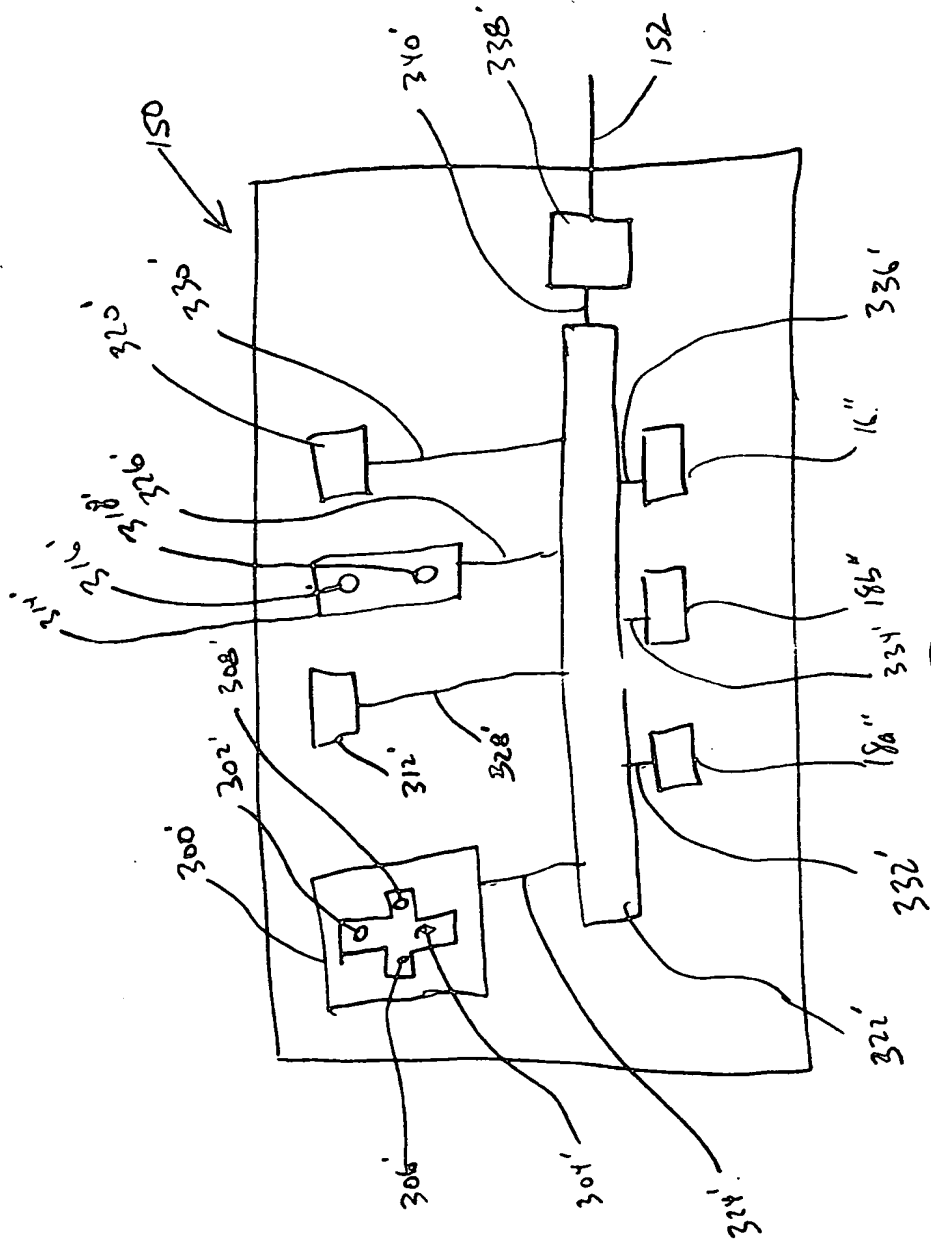


FIG. 12

102290" 6848860

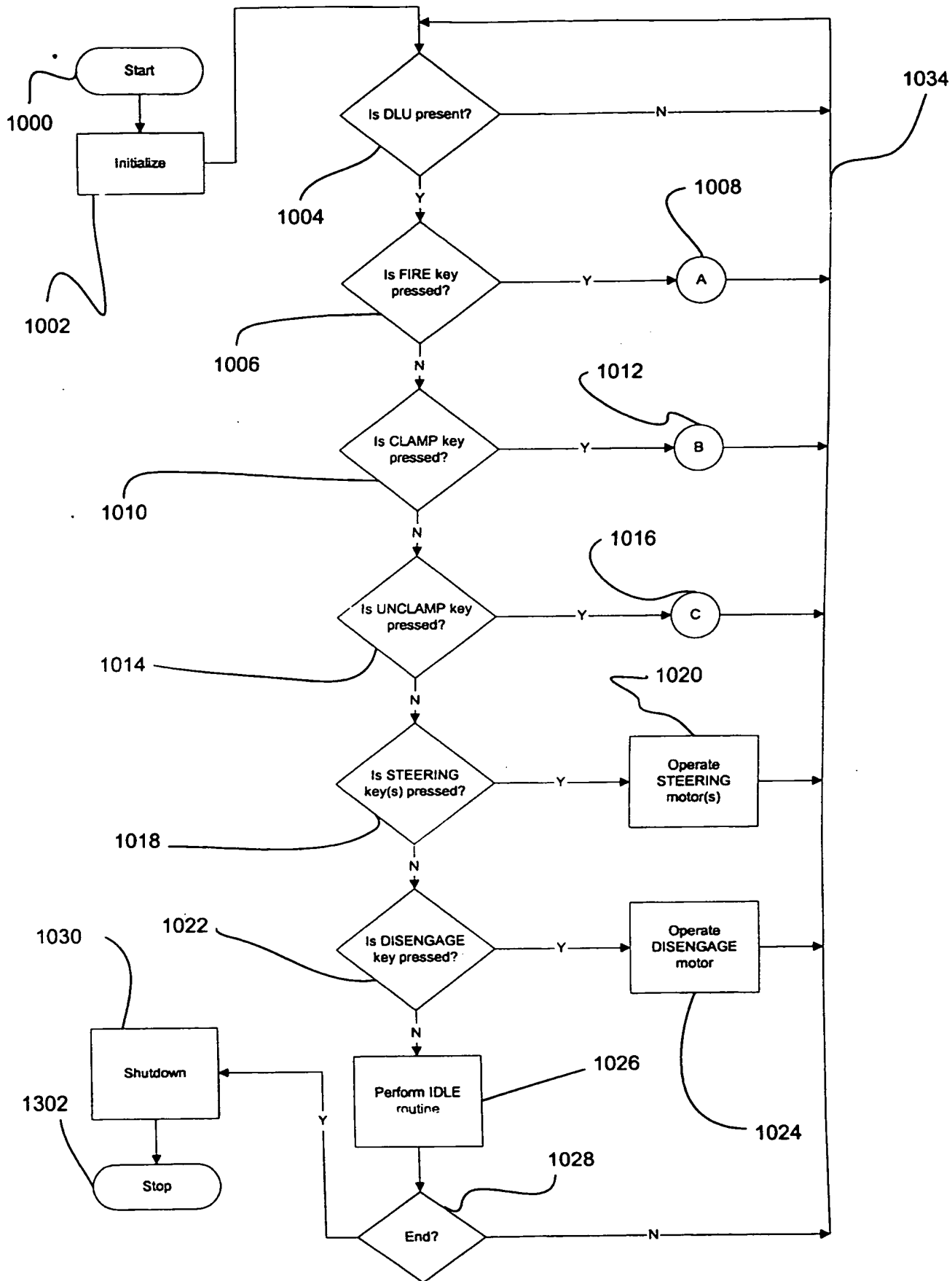


Figure 13

102290" 58428860

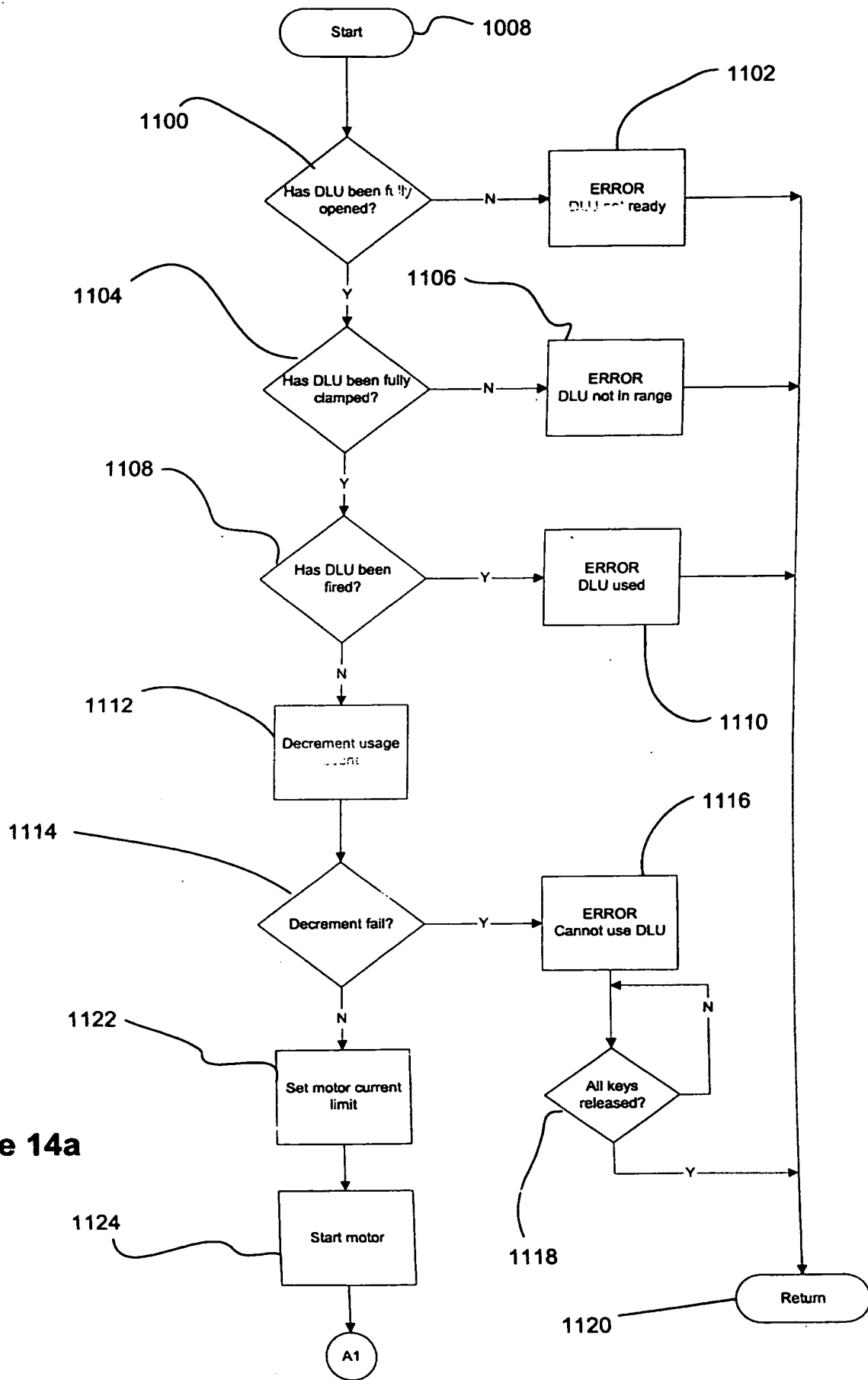


Figure 14a

0908790 06201

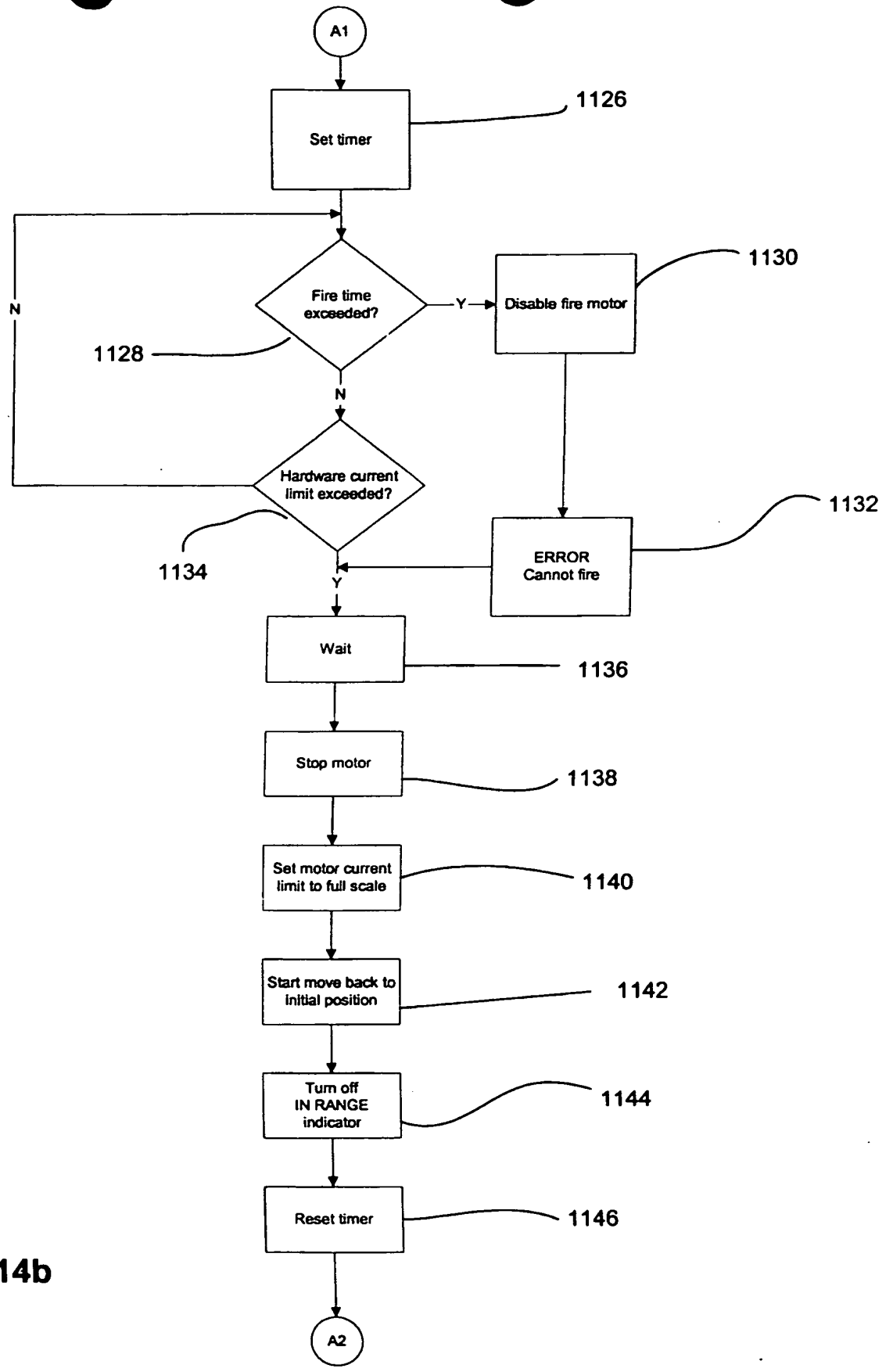


Figure 14b

0988786201 6878860

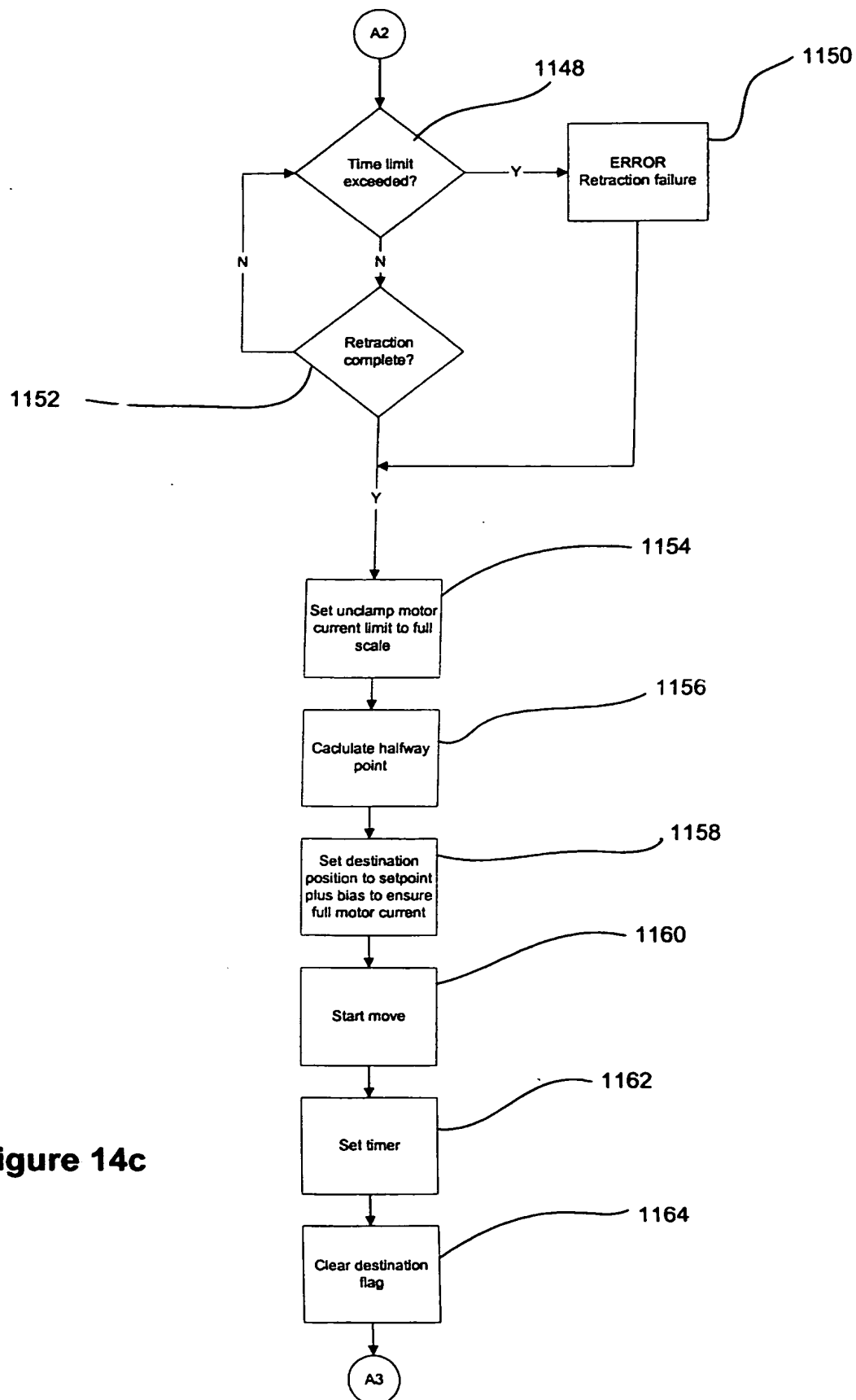


Figure 14c

098785 63428860

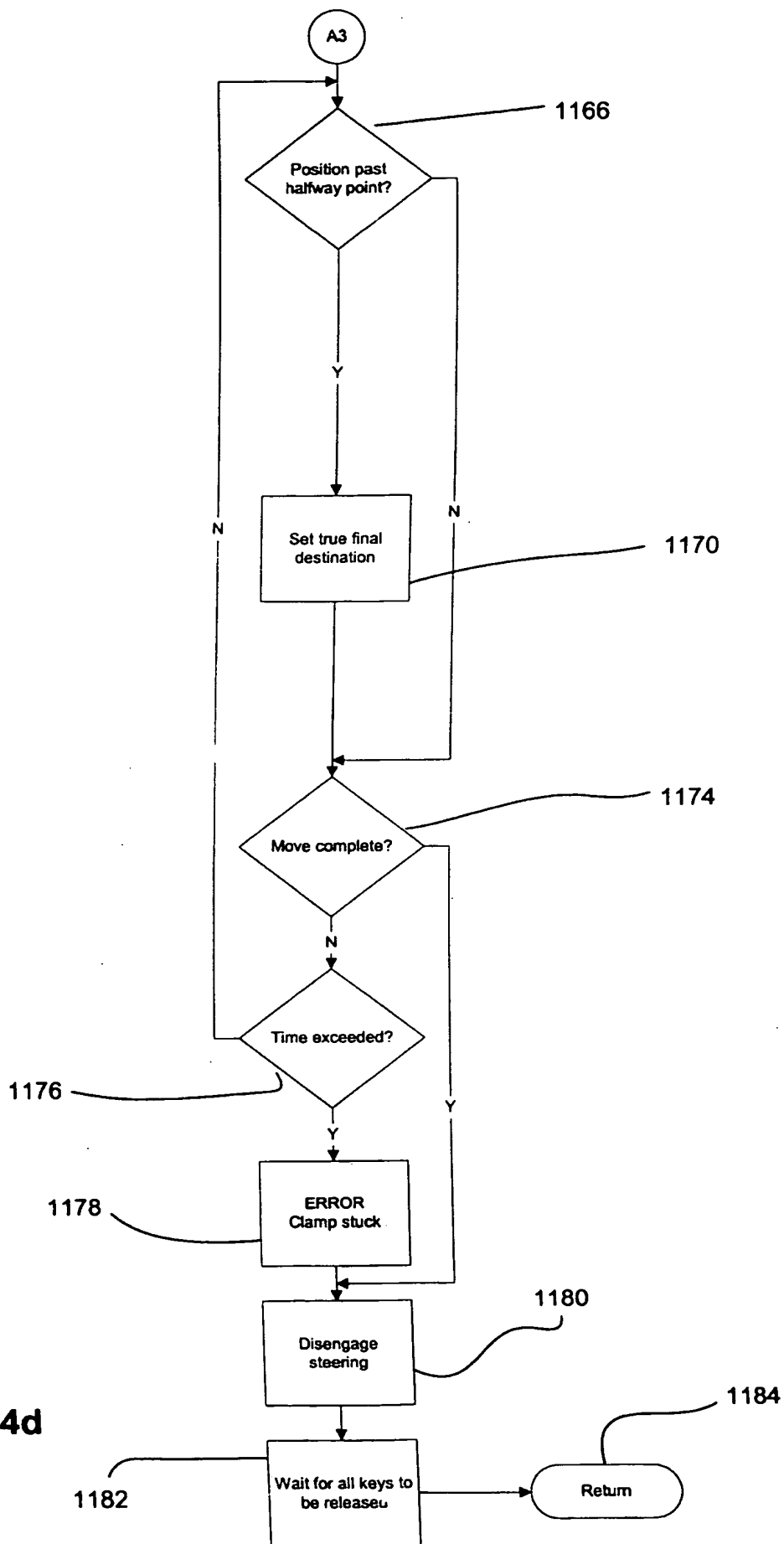


Figure 14d

0988789 062201
"02290" 6848860

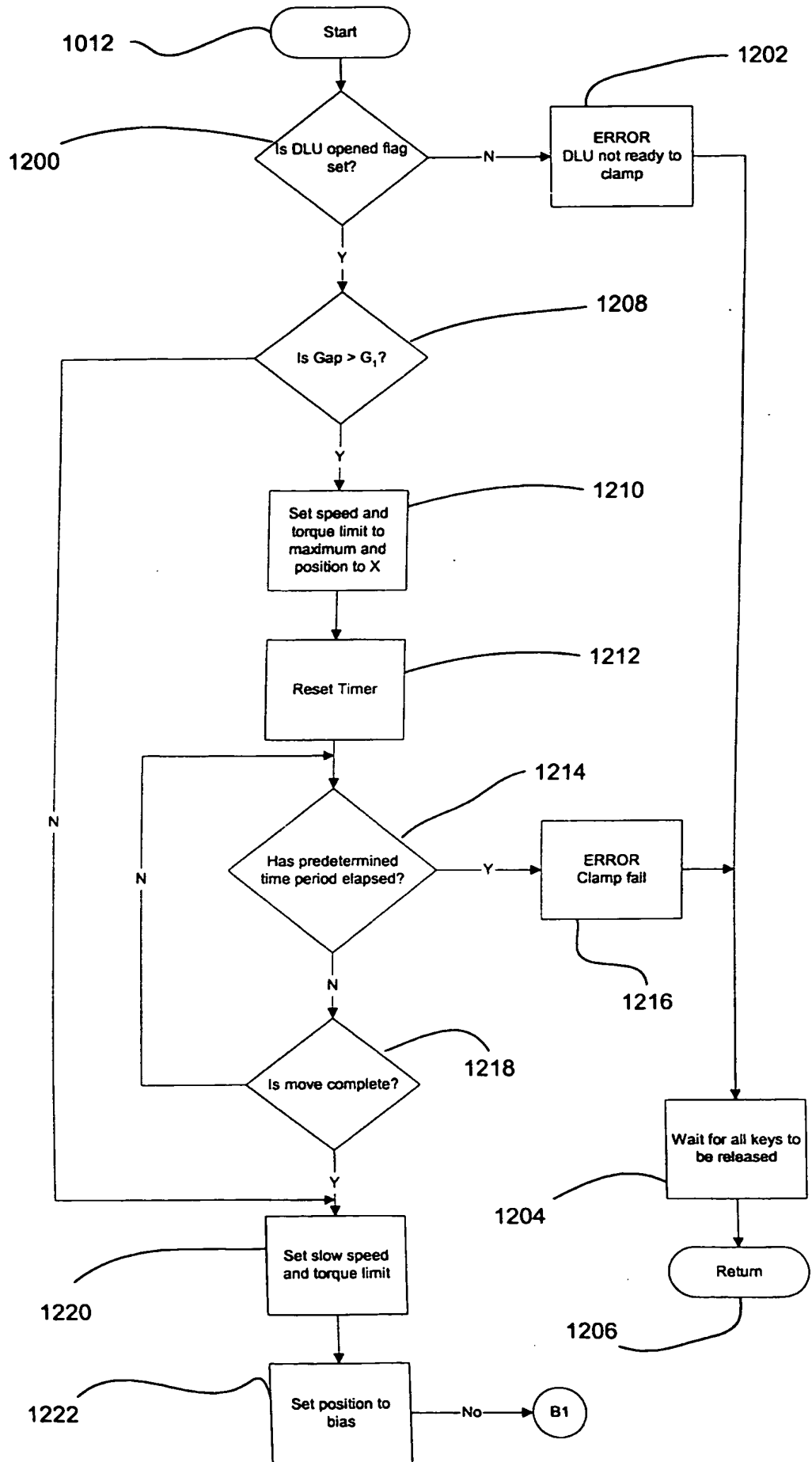


Figure 15a

090607Z JUL 80

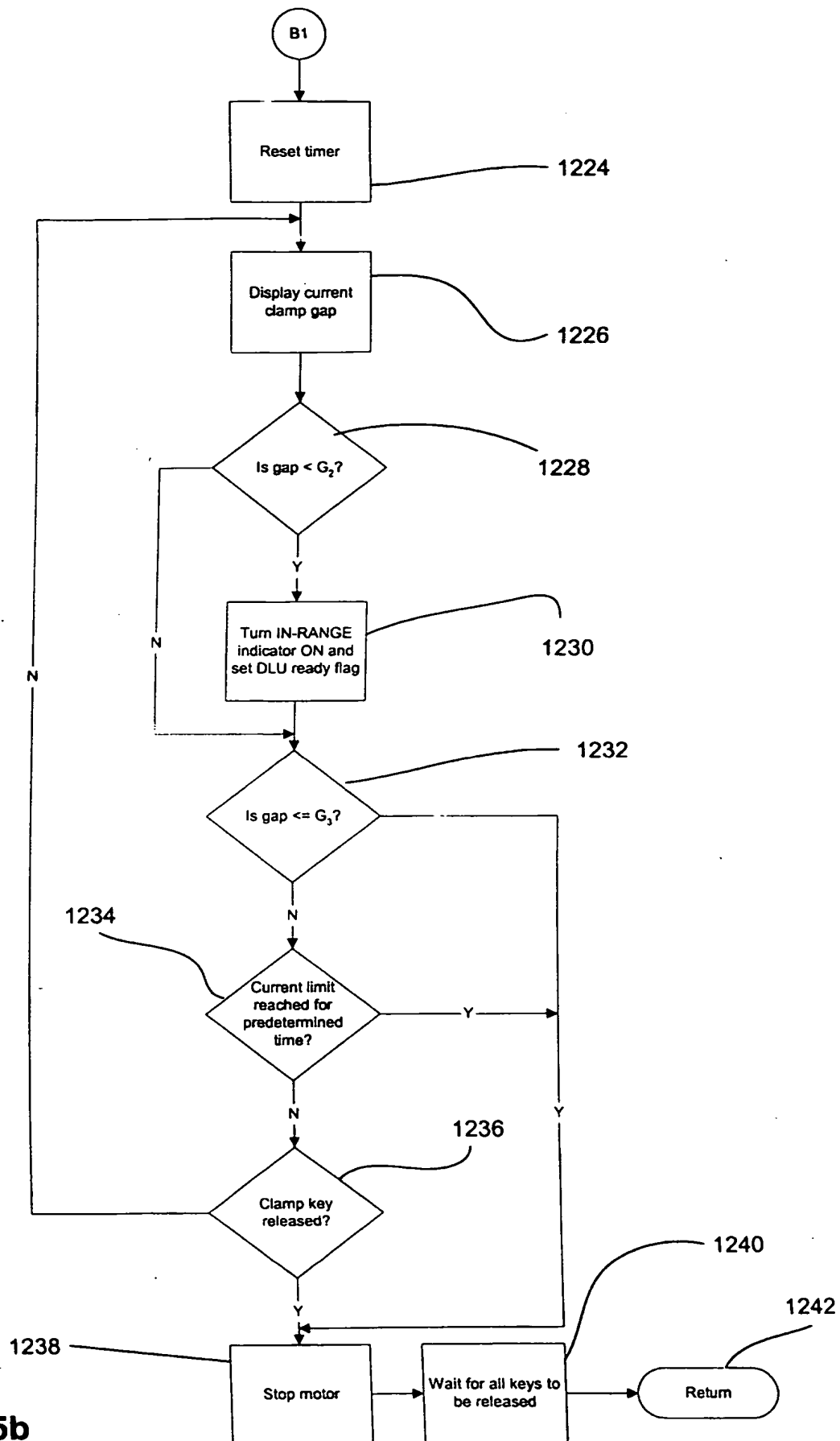


Figure 15b

00000000000000000000000000000000

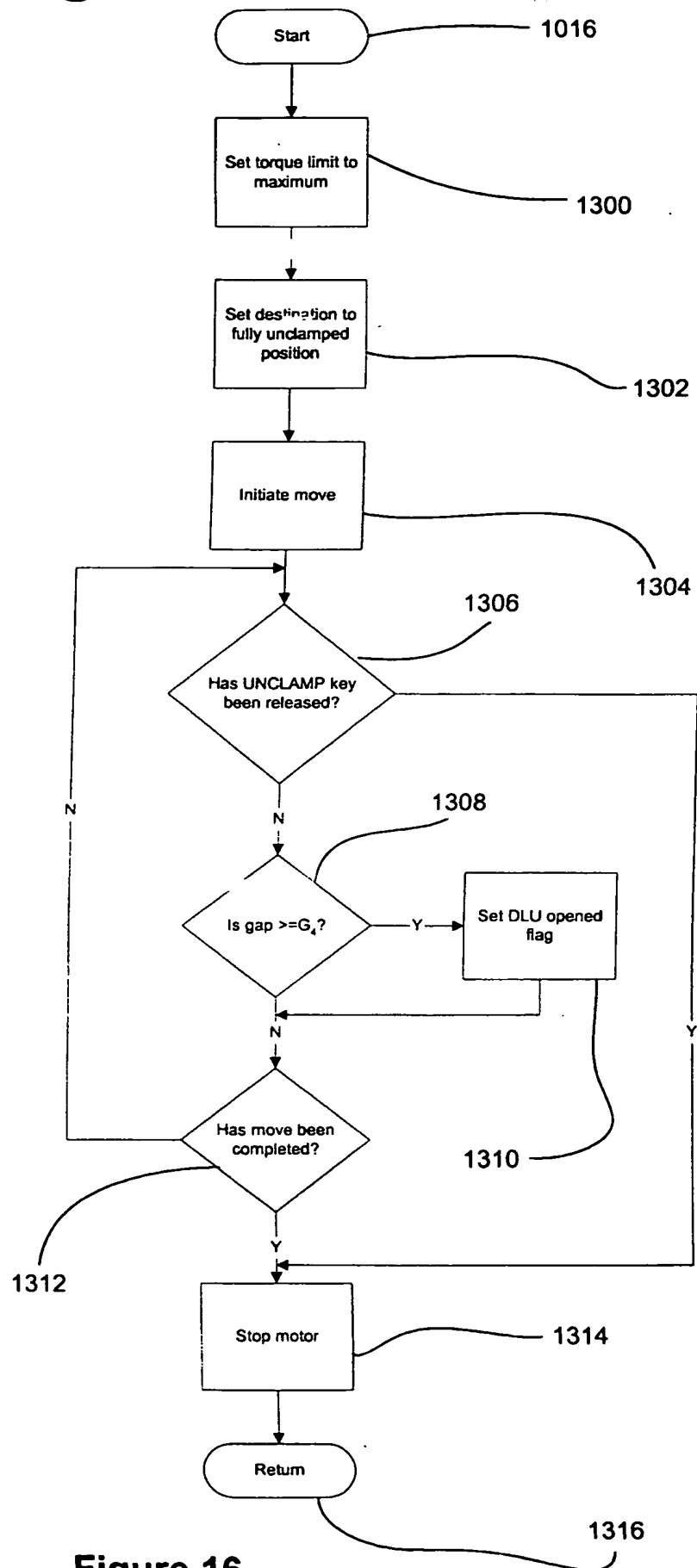


Figure 16

09887789 062201

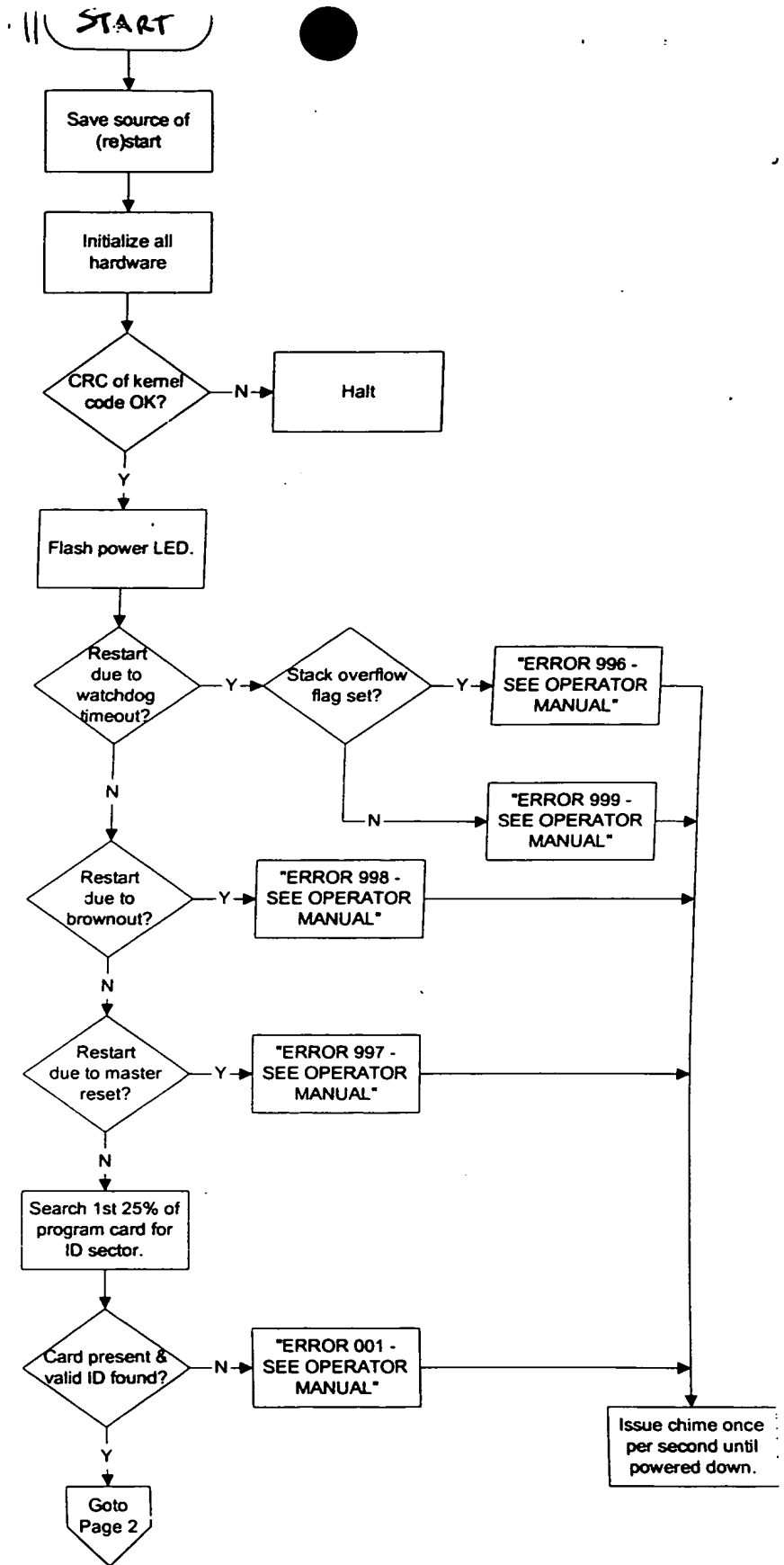


FIG. 17a

09887789-062201

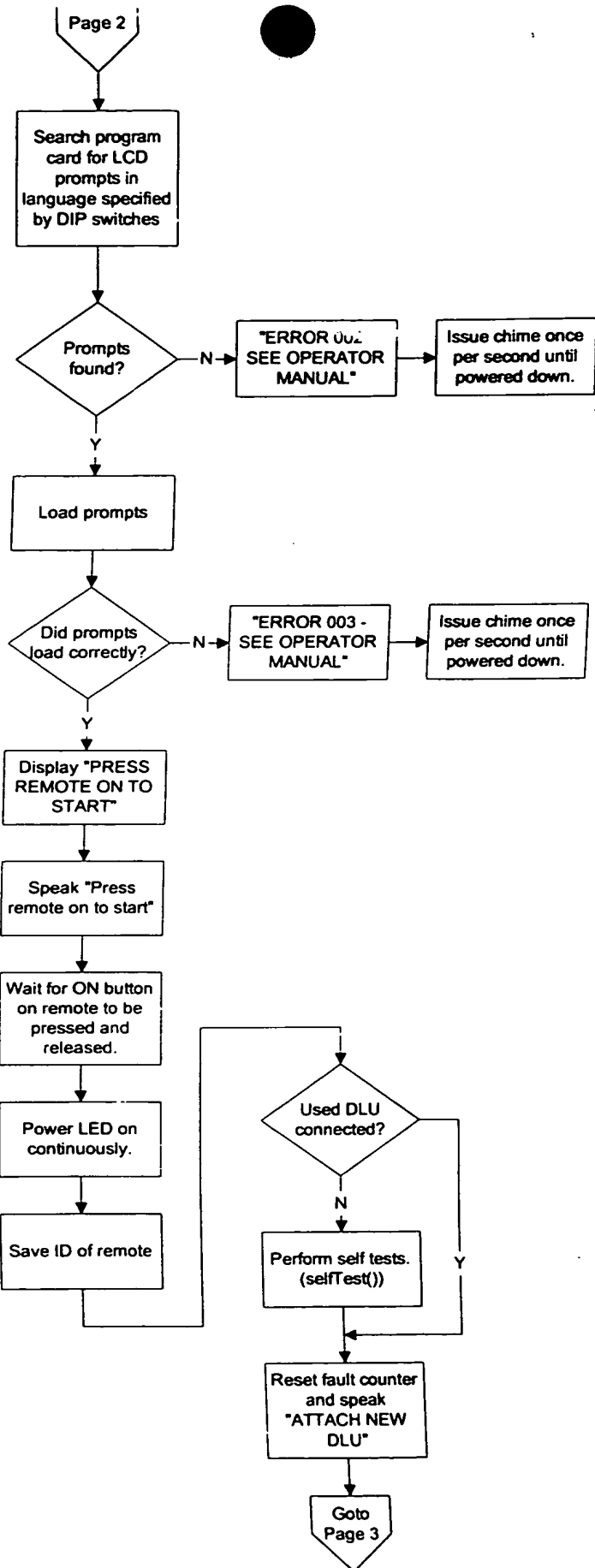


FIG. 17b

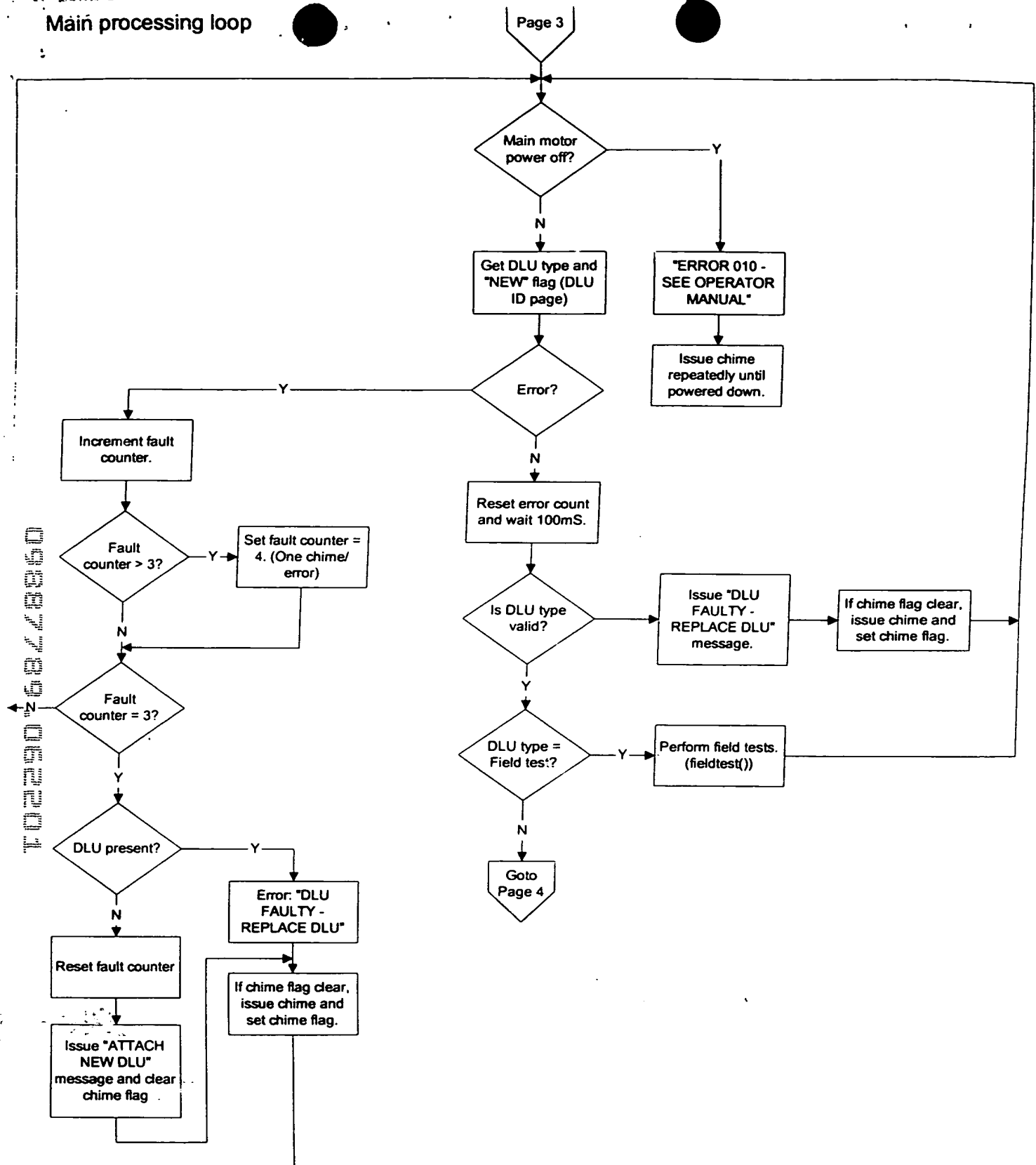


FIG. 17c

102290" 684/8860

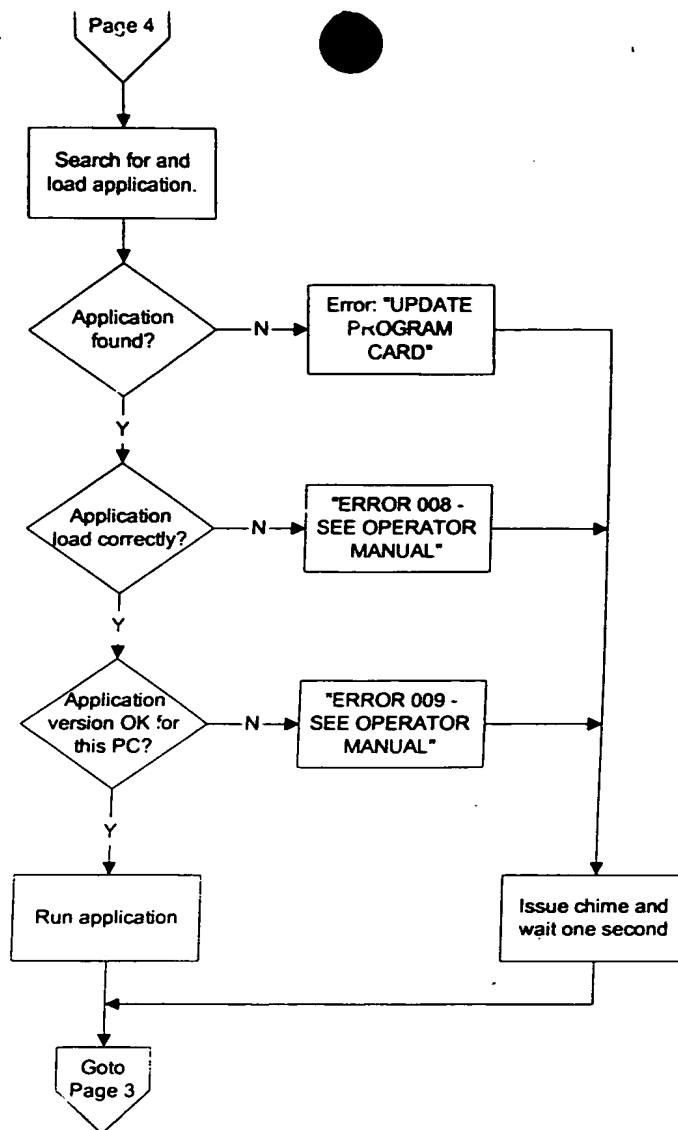


FIG. 17d

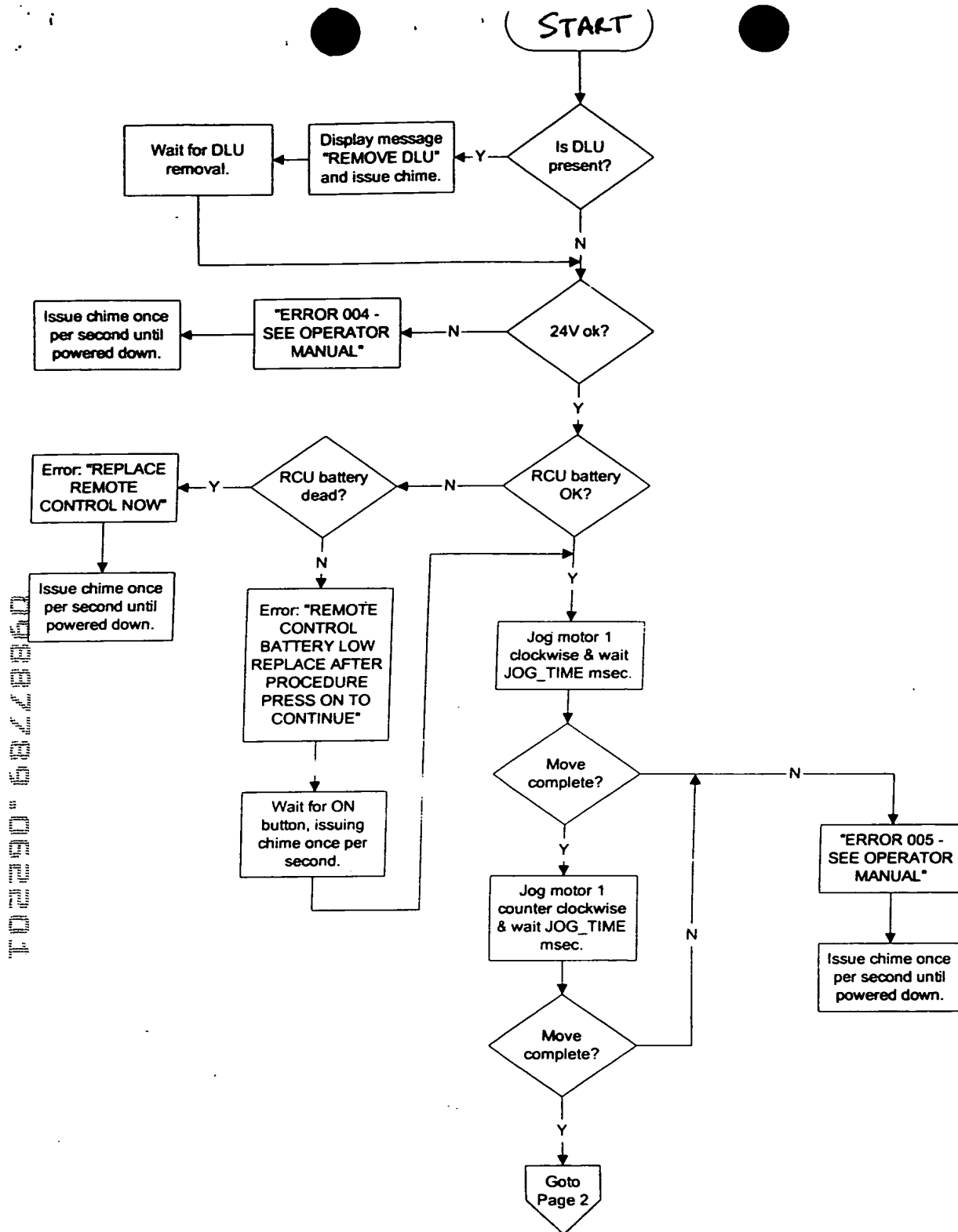


FIG. 18a

09887789 062201

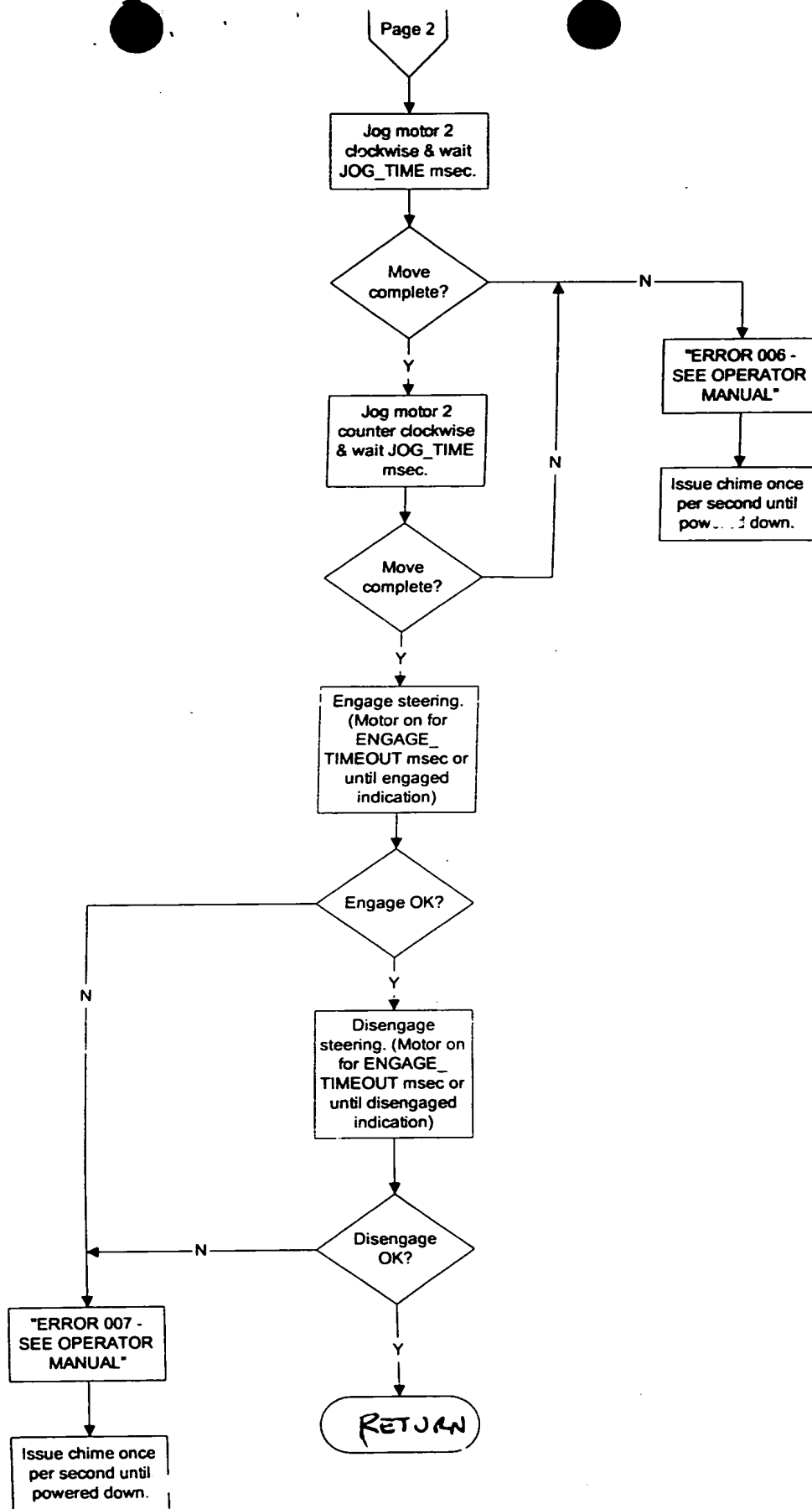


FIG. 184

0988789 06204
T02290" 6848860

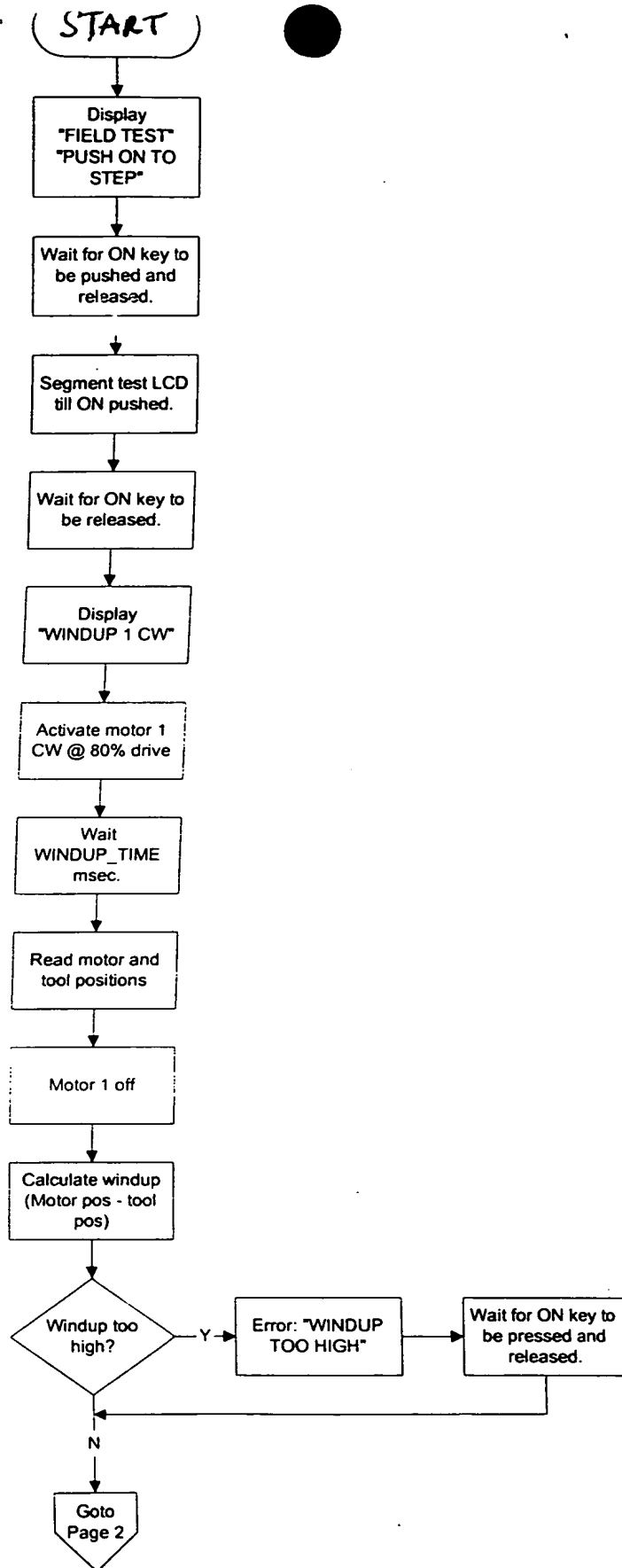


FIG. 19a

09887789 062201

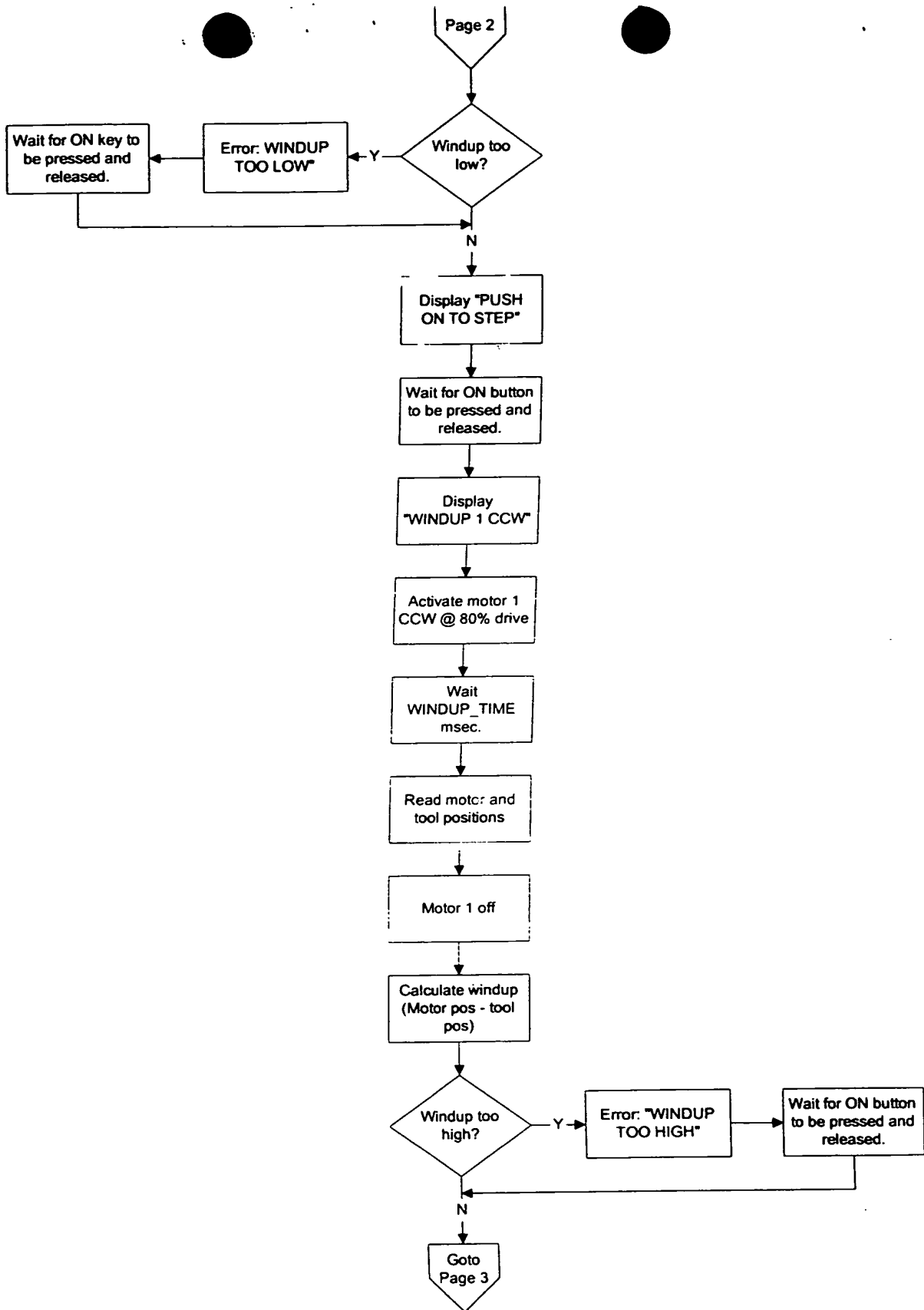


FIG. 19b

09887789.062001

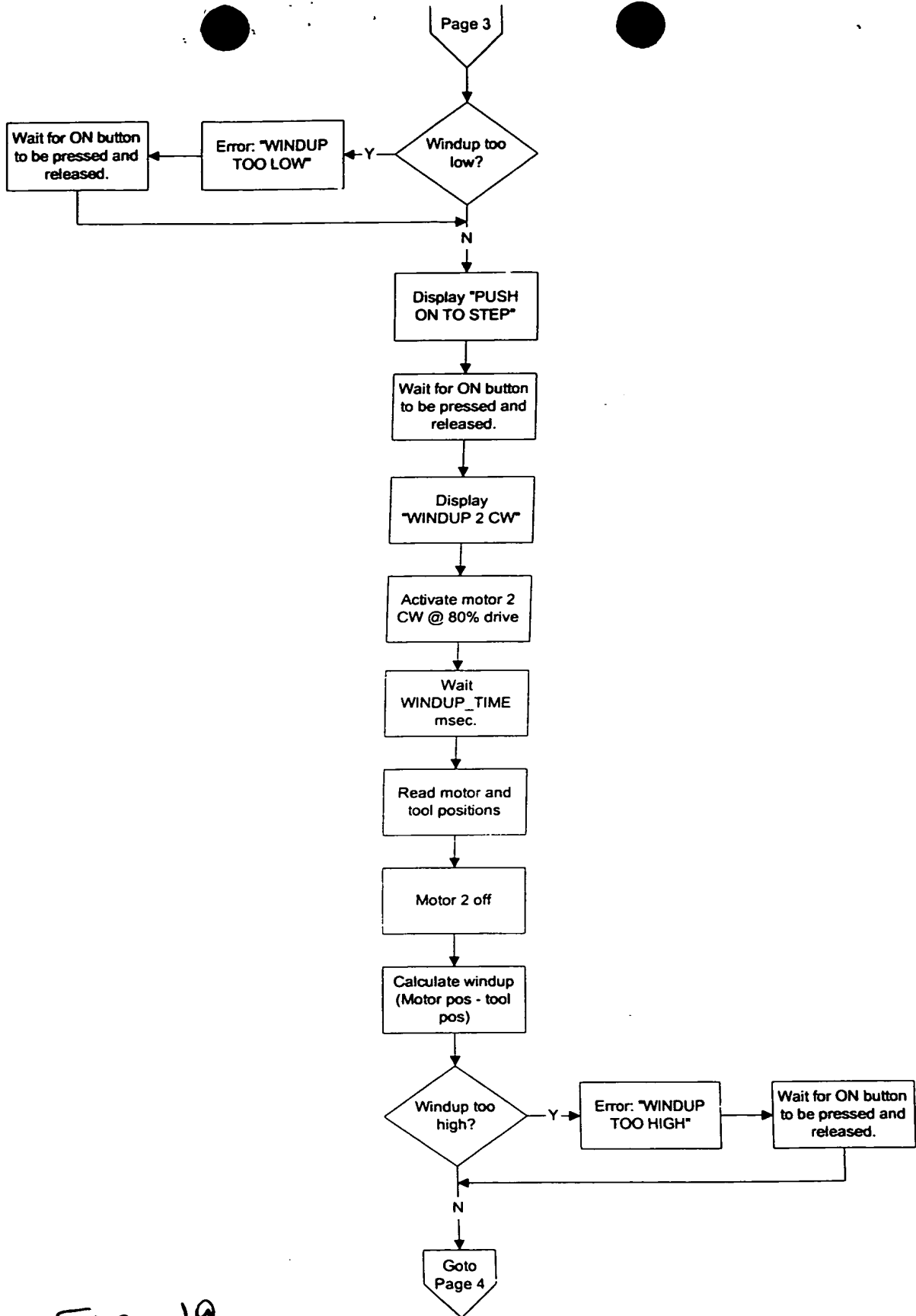


FIG. 19c

09887789 062204

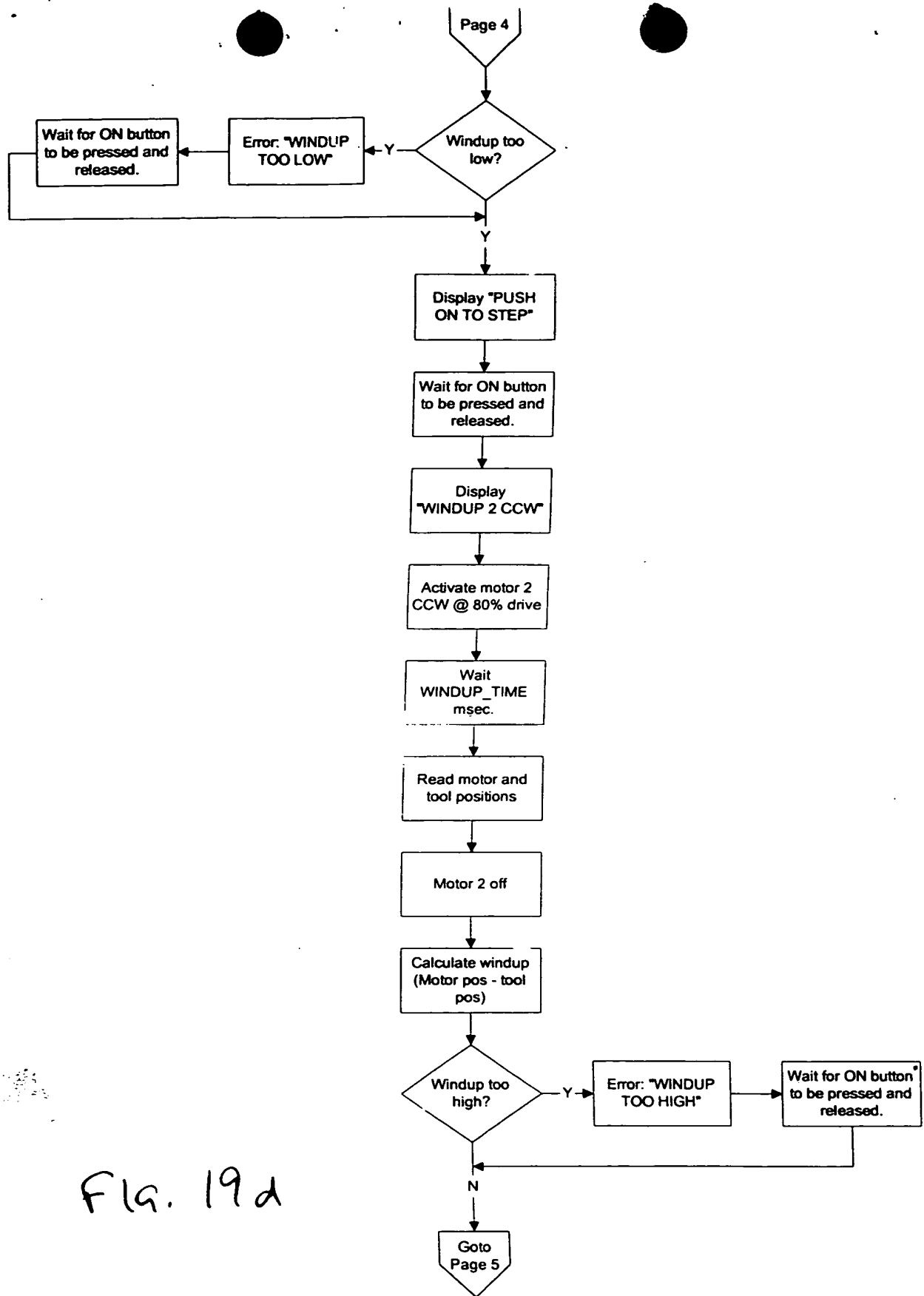


Fig. 19a

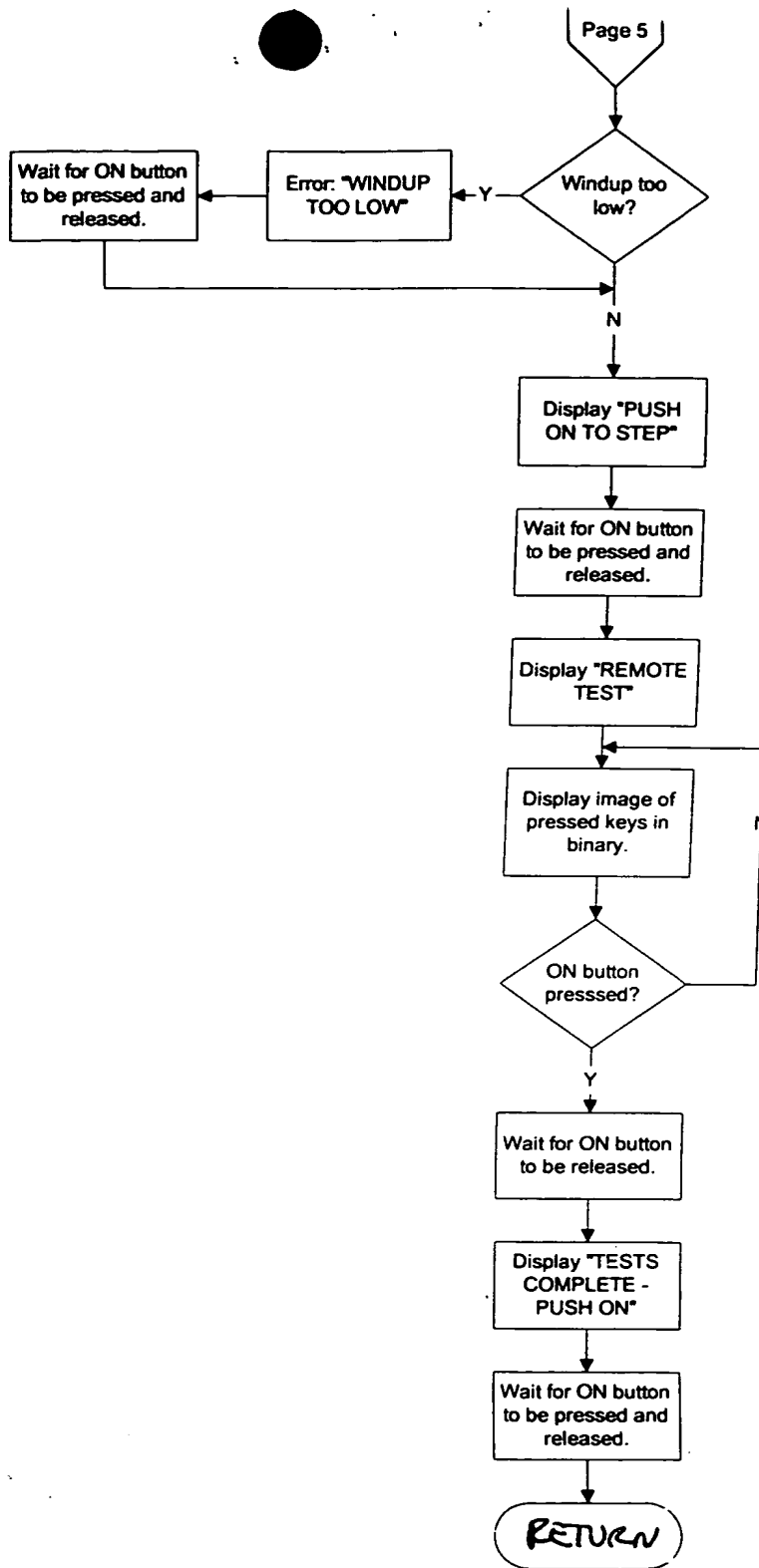


FIG. 19e

09687789 06201

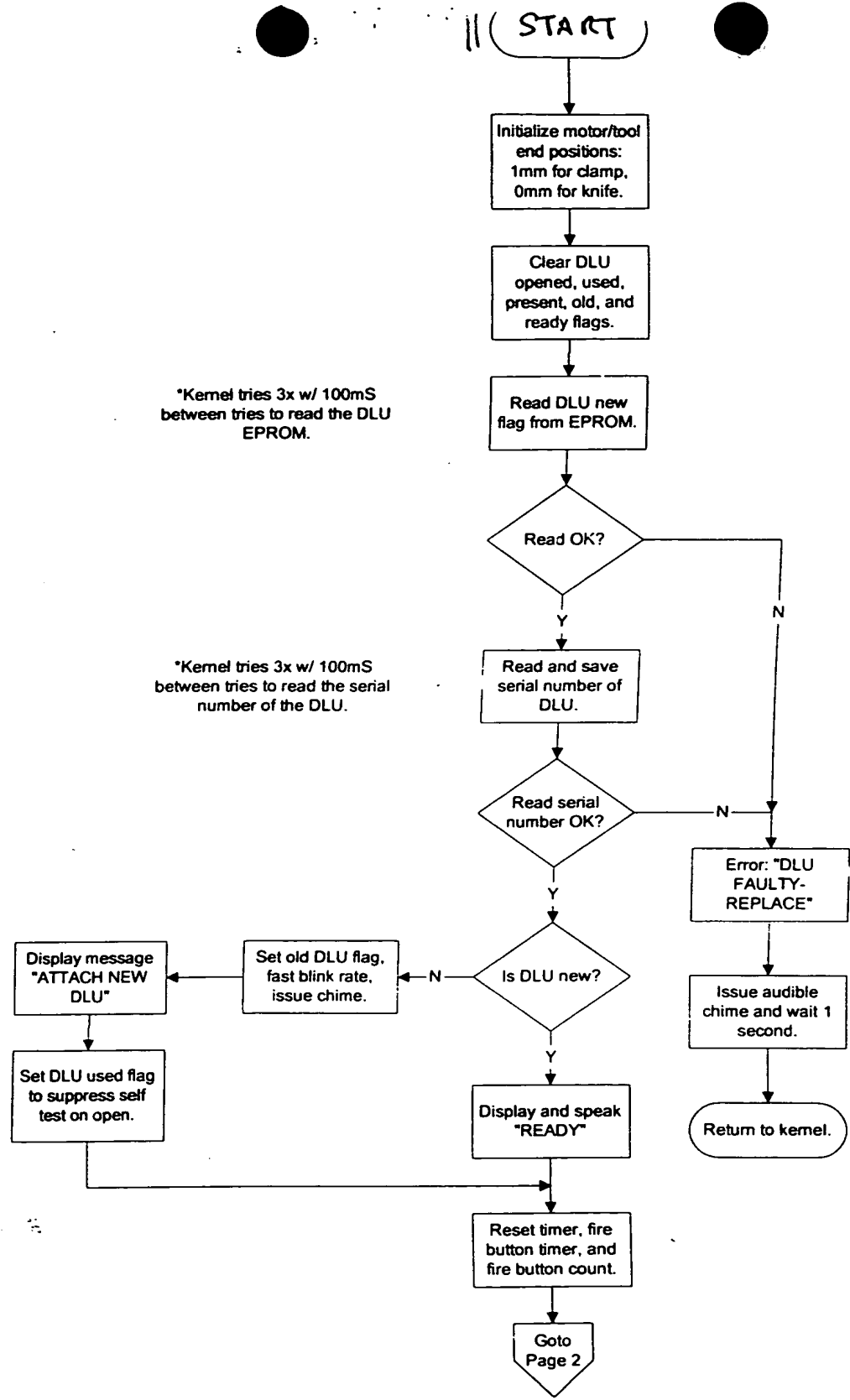
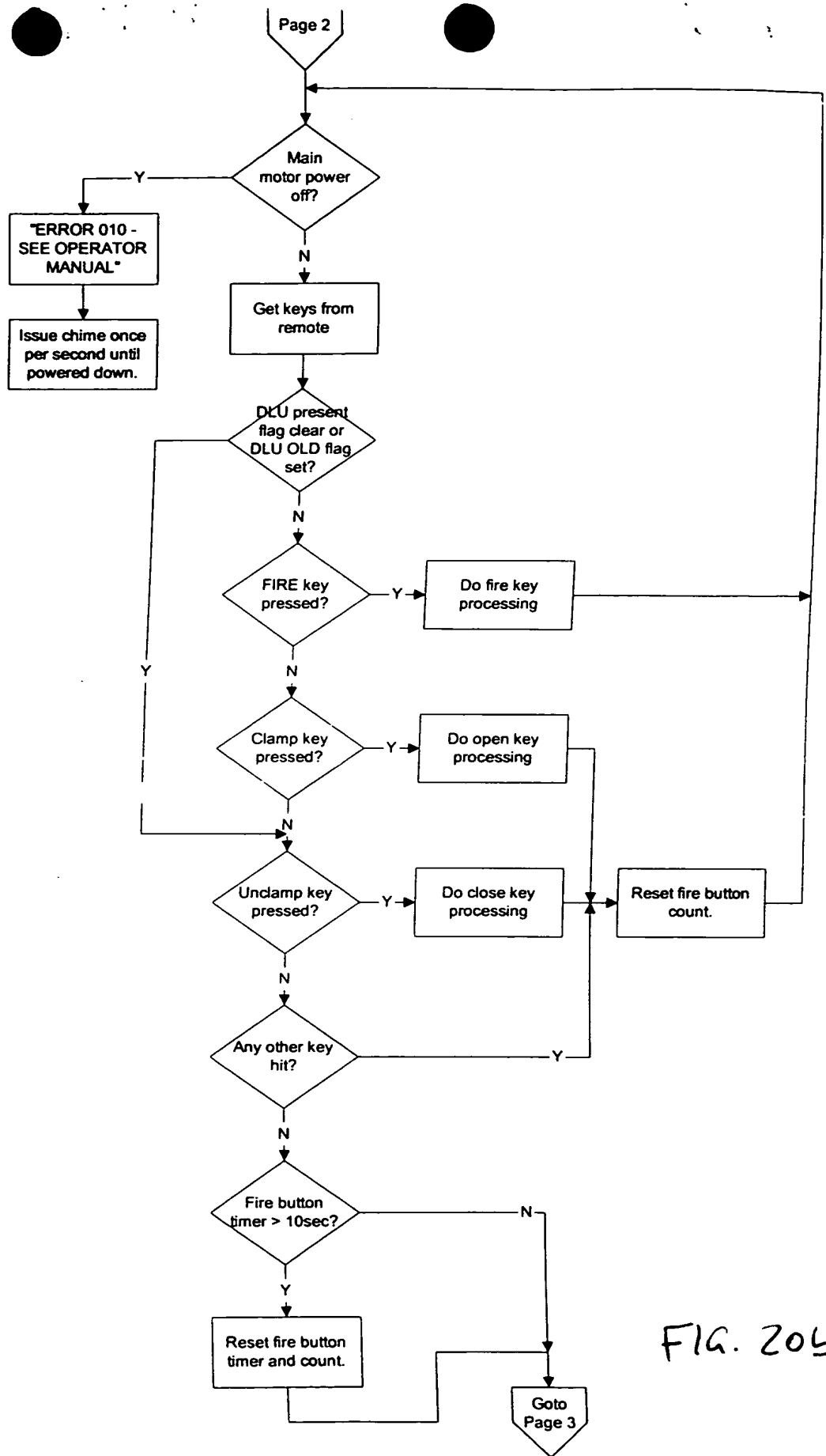
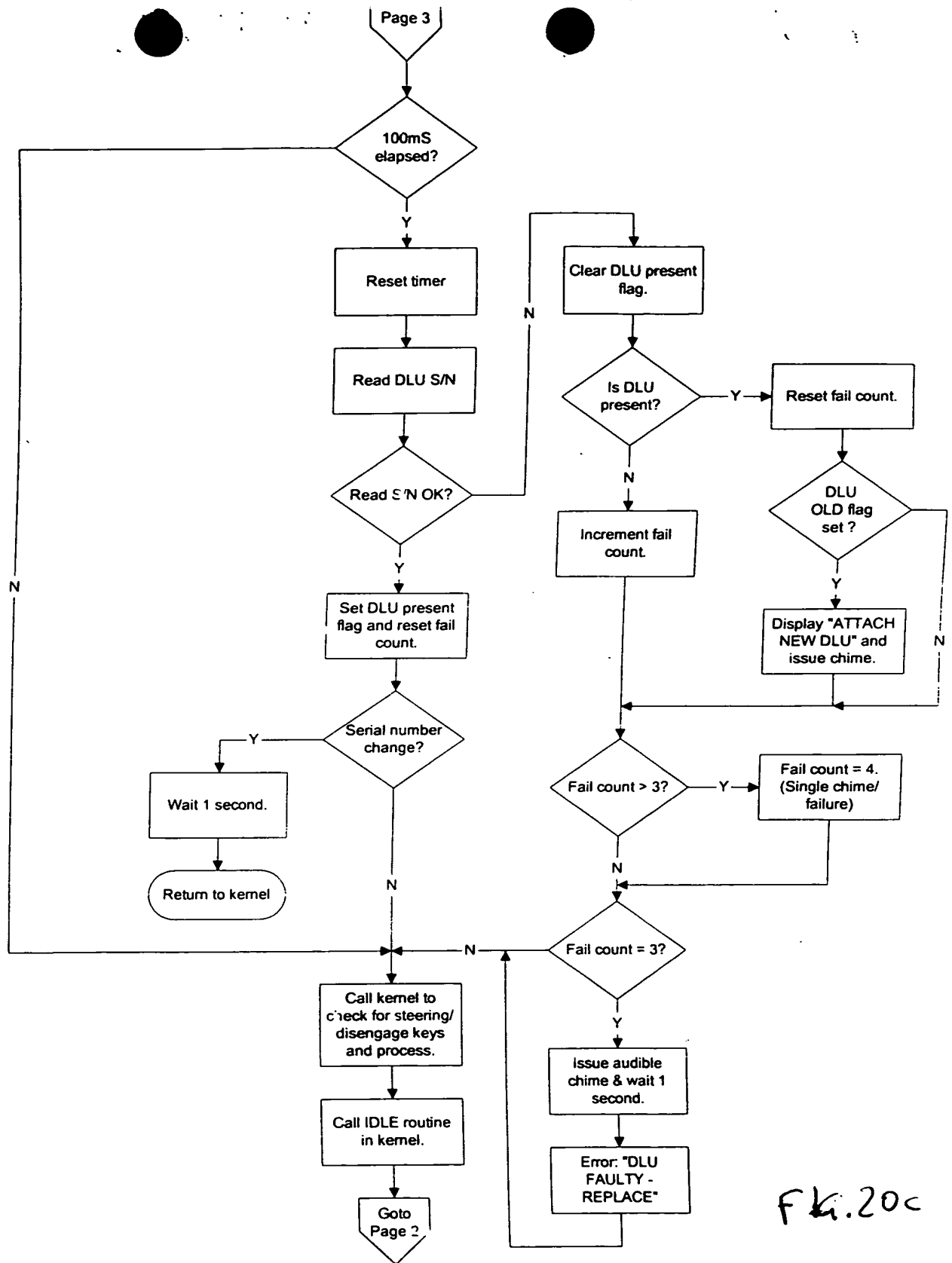


FIG. 20a



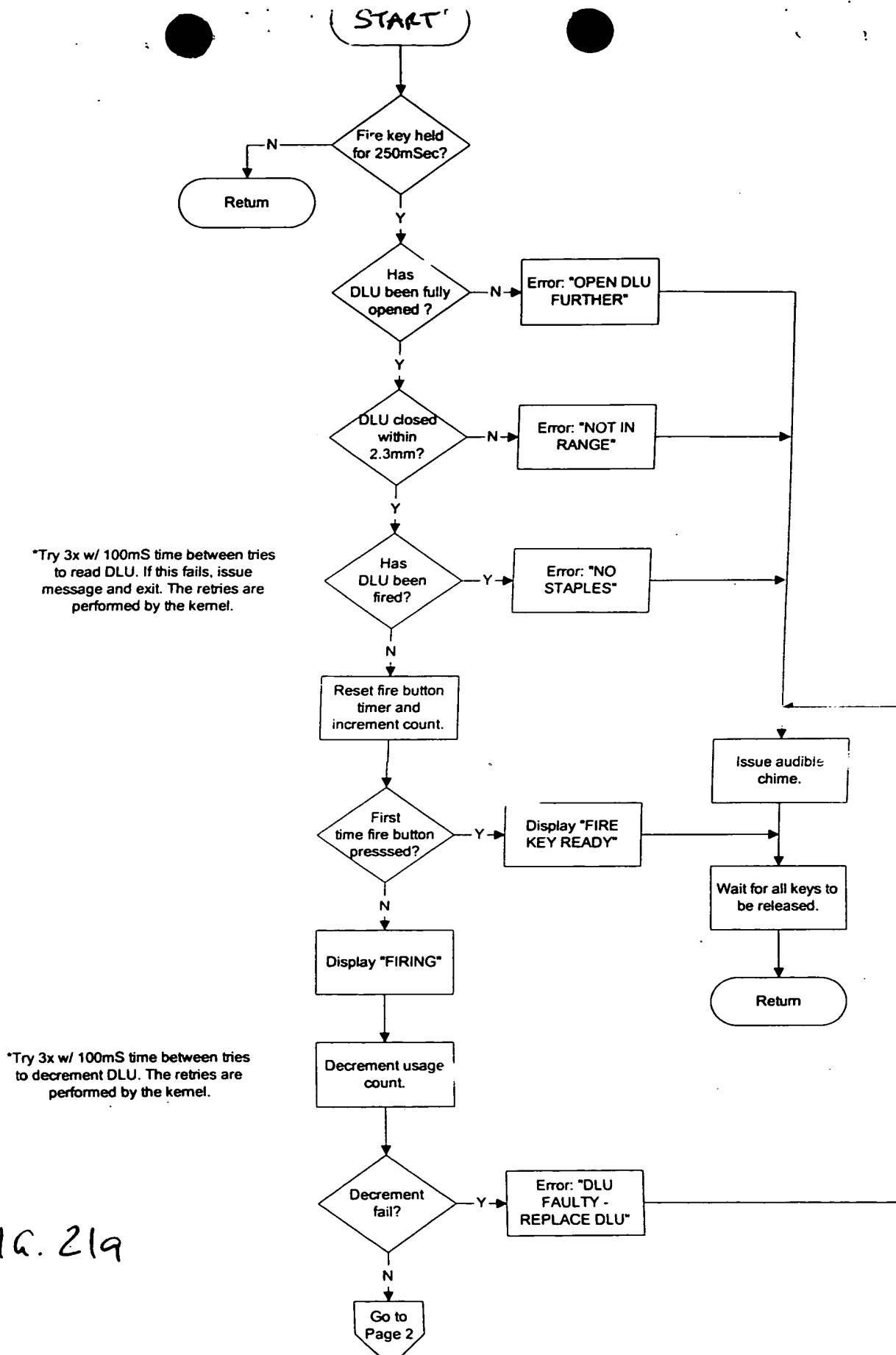
0900789-06201



Fk.20c

09887789-06200

FIG. 21a



09887789 02201
 WINDUP threshold is defined as
 SHAFT_WINDUP_MAX

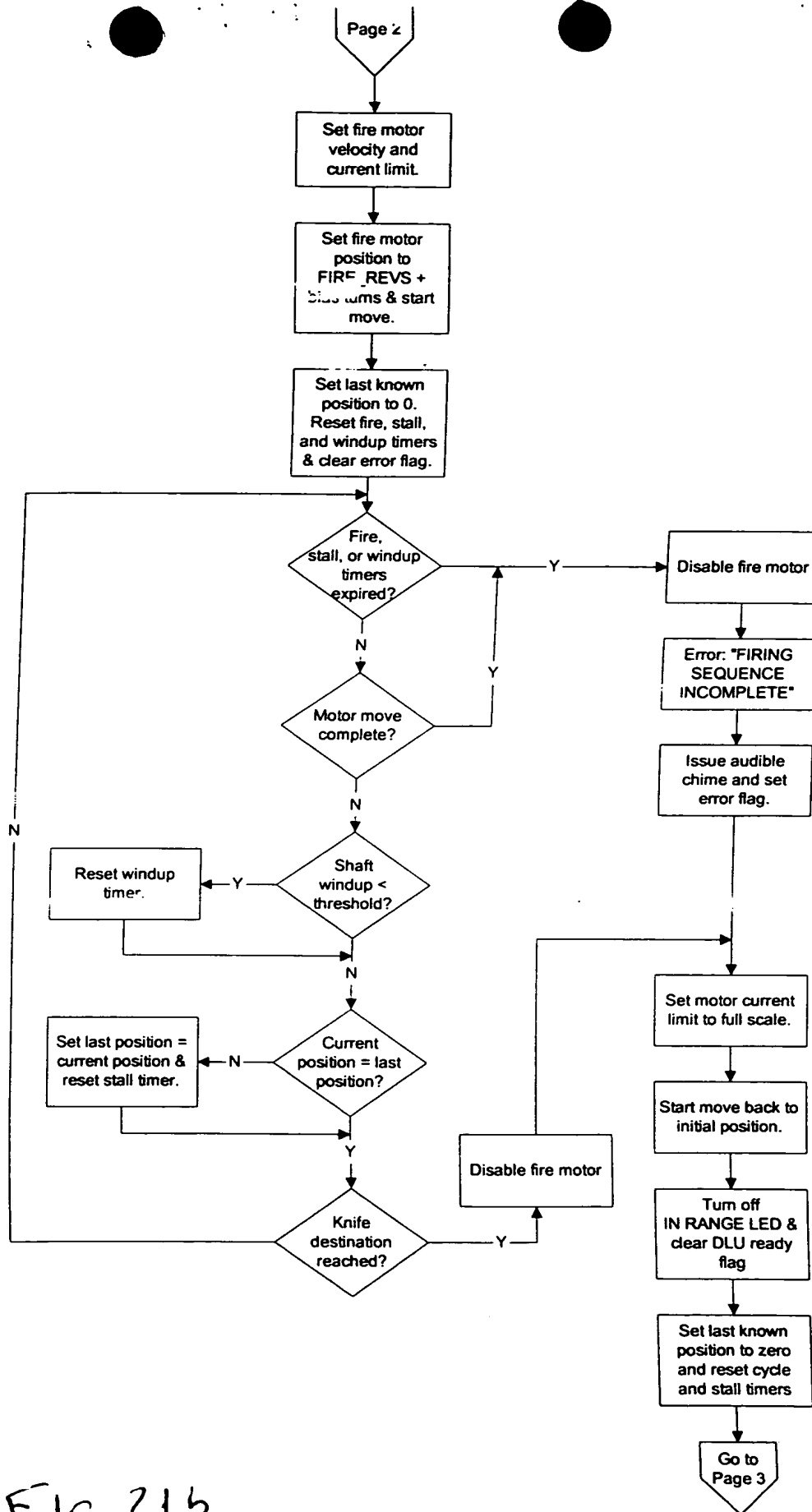


FIG. 21b

09887789 062201

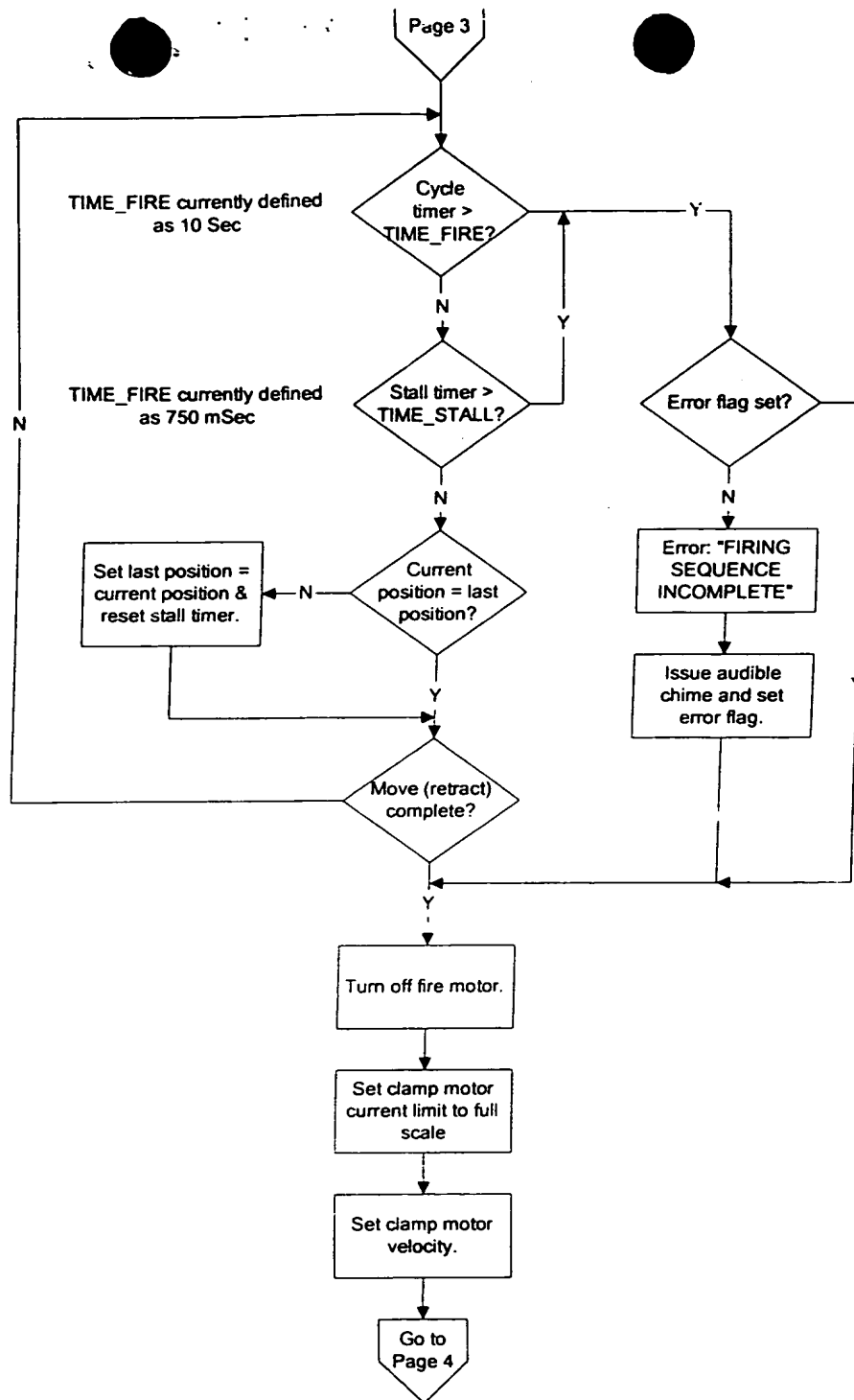


FIG. 21c

09087789-062201

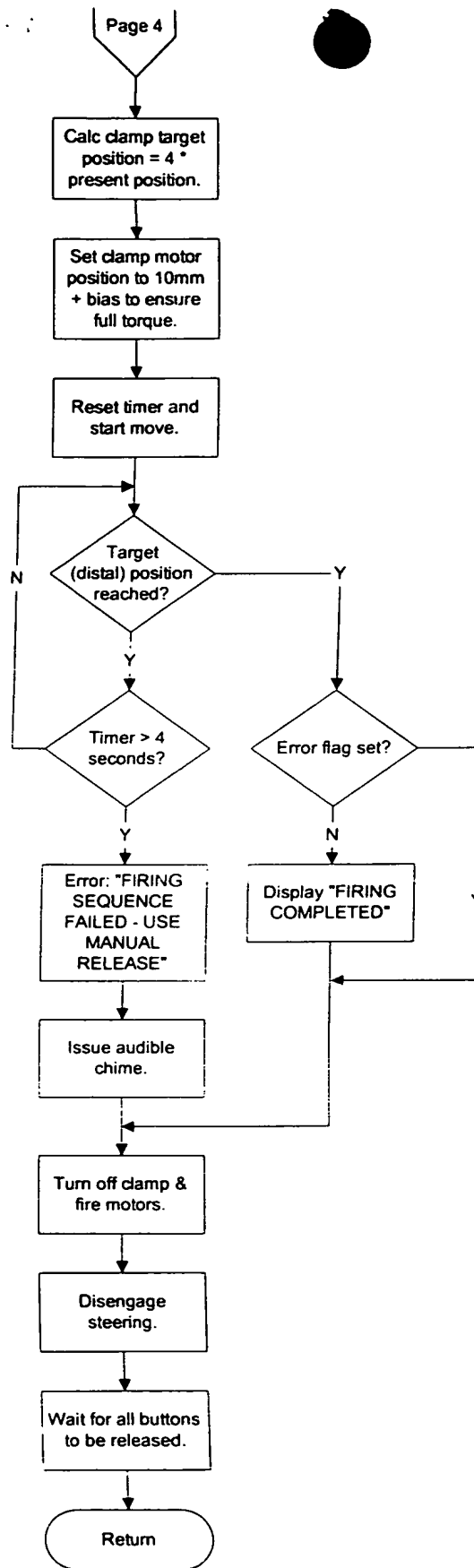
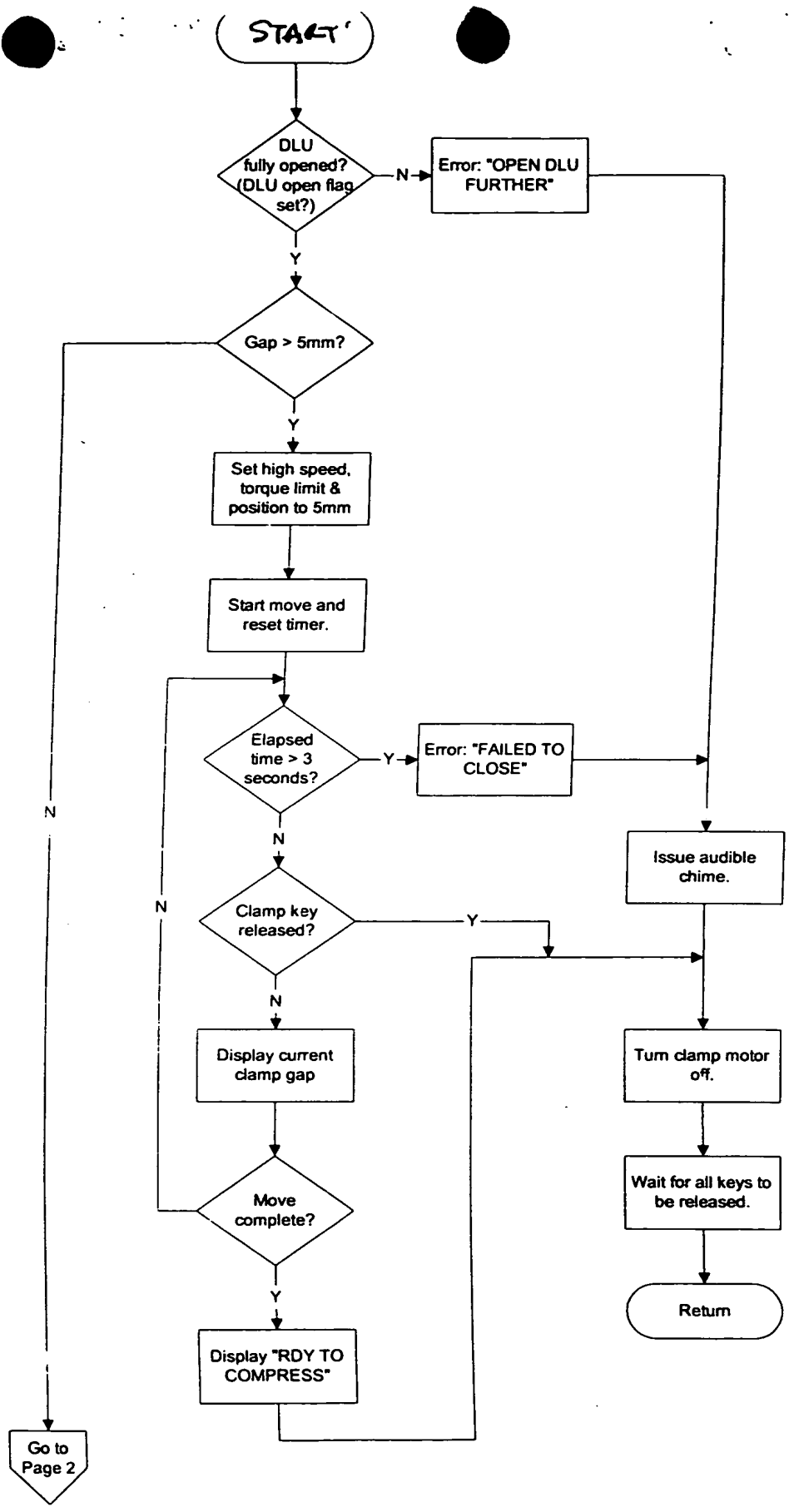


FIG. 21d

0988789-06221
T02290" 68/28860

FIG. 22a



09087789 06201

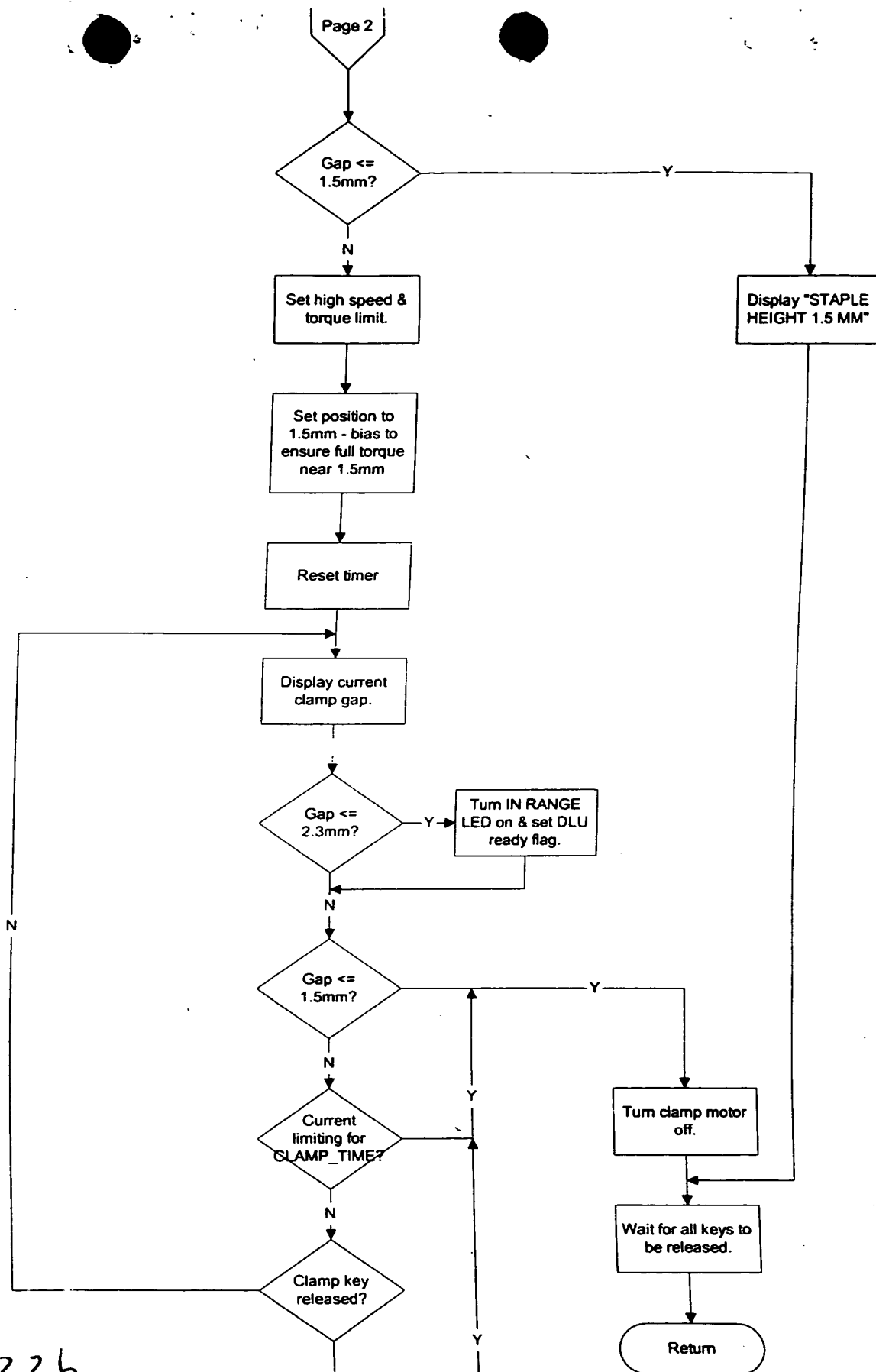
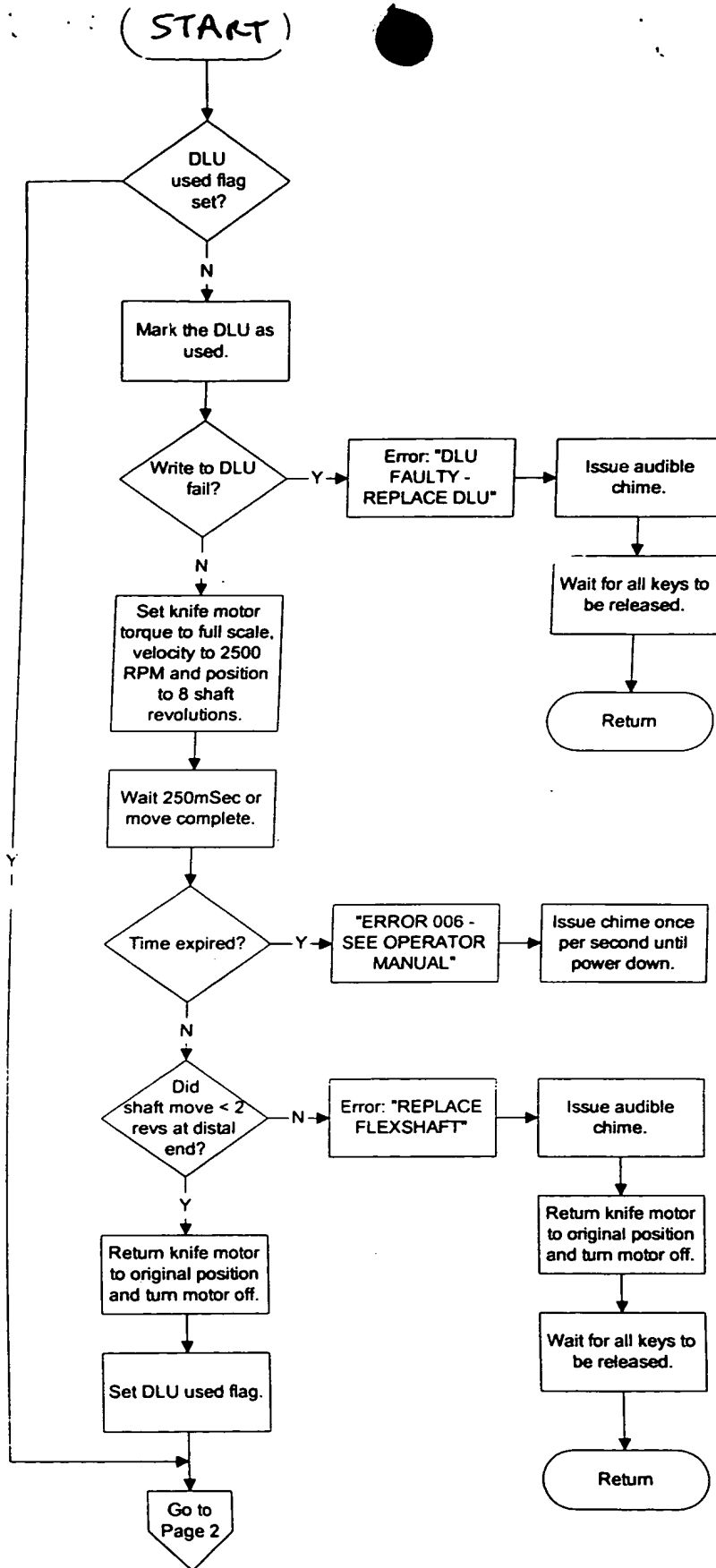


FIG. 22b

T02290" 6822860

FIG. 23a

*The kernel tries 3x w/ 100mS time between tries to write DLU.



1002290".68428860

FIG. 236

